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USER'S GUIDE

EG3210
PROGRAMMABLE GRAPHICS ADAPTOR

(FOR GENIE Ⅲ)

Eaca®

CONTENTS

PGA USER'S GUIDE

1. Introduction

This optional card is designed for adapting to the Interface I card of your GENIE III system. This programmable Graphics Adaptor (PGA) facilitates user-programmable graphic/character sets. It is supported by the software utility named the User Programmable Graphics/Character Generator. It allows us to create different character sets and to store them in a disk. When used, a specified character set can be loaded into the 1K byte video memory on this PGA card.

2. **Installing the PGA**

The PGA card must be plugged onto the Interface I card before the utility of Programmable Graphic Generator can be used. It may be installed as follows.

(1) Turn OFF the power of the System. Remove the rear cover of the cabinet. You will see the electronic boards in the card cage. See Fig. 1(a).

(2) Remove the Interface I card from the card cage by using the PCB ejectors. See Fig. 1(b).

(3) Find the CRT controller chip, HD 46505. Remove this 40-pin IC from the Interface I card, and plug it into the IC socket on the PGA card. See Fig. 2.

(4) Find the two 20-pin socket strips on the Interface I card. Plug the PGA card onto these two socket strips and the 40-pin IC socket (originally for the CRT controller chip on the Interface I card).

Fig. 1(a) Rear Panel of GENIE III System

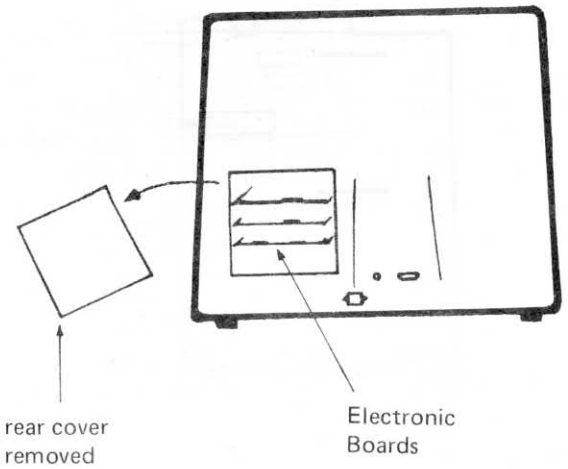


Fig. 1(b) Interface I Card

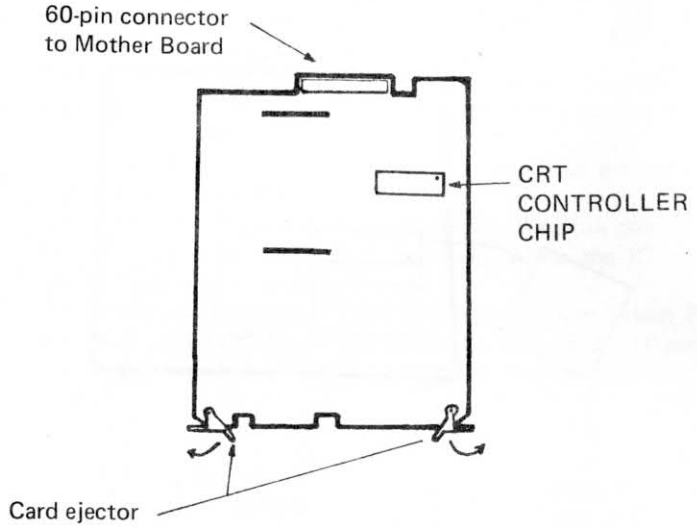
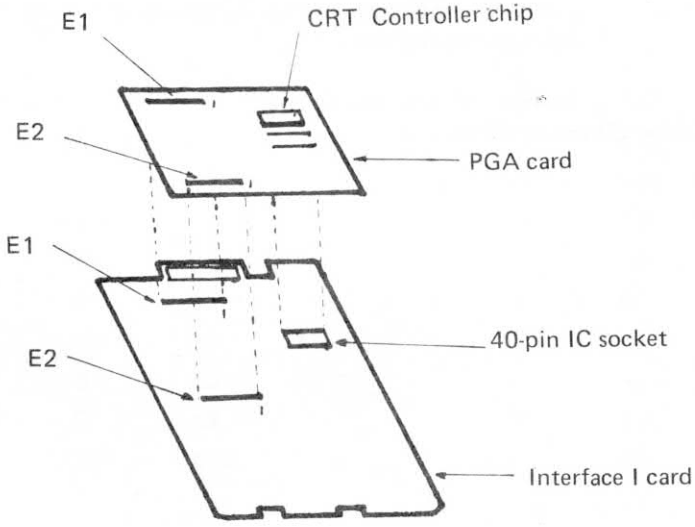


Fig. 2 Installing the PGA onto the Interface I Card



(5) Place the Interface I card back into the card cage ensuring that this double-storied card has been firmly plugged into the 60 pin connector of the Mother Board in the card cage. Put back the rear cover.

Then, proceed to try the software utility of the Programmable Graphics/Character Generator.

3. **Some Technical Aspects**

4.1 **Pin Assignments of the Two 20-pin Connector Strips (PGA)**

Pin	E 1	Pin	E 1
1	WR	11	A15
2	IORQ	12	A13
3	MREQ	13	A11
4	A2	14	A1
5	A14	15	A3
6	A12	16	A4
7	AØ	17	A6
8	A5	18	A7
9	A9	19	-5V
10	AIØ	20	A8

Pin	E 2	Pin	E 2
1	LD2	11	VDG
2	LD4	12	GND
3	LD5	13	GND
4	LD3	14	GND
5	LD1	15	PMG
6	LDØ	16	RD
7	PWAIT	17	BKØDIS
8	S/L	18	+5V
9	PGC	19	12.875MHZ
10	+12V	20	BK3

4.2 Component Layout of the PGA