# A GUIDED TOUR OF PERSONAL COMPUTING



By the Sorcerer of



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# DOPPEL

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By The Sorcerer of Exidy

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The Sorcerer of Exidy

By

WELCOME ABOARD

TRAVEL ITINERARY

INTRODUCTION, SCOPE OF MANUAL		1
PACKING AND UNPACKING	5	
CONTENTS OF BOX, HOOK-UP		
INSTRUCTIONS, RETURN AUTHORIZATION,		1
TURN-ON		•
TRAVEL AIDS	13	1
DESCRIPTION OF SYSTEM COMPONENTS,		•
INTRODUCTORY PROGRAMMING		
<b>BON VOYAGE</b>	19	
DETAILED OPERATION, KEYBOARD		•
DESCRIPTION		•
EXPERIENCED TRAVELER	23	1
POWER ON MONITOR DESCRIPTION		

## POWER-ON MONITOR DESCRIPTION. BATCH SYSTEM, RELOCATING THE STACK SPECIAL OFFERING USER-DEFINED GRAPHICS

**APPENDIXES** 

FURTHER INFORMATION..... SERIAL/PARALLEL PORTS, CASSETTE I/O. VIDEO, BUS CONNECTION A. MEMORY MAP

E. ASCII CHARACTER CODE	 	• • •	 	 ٠.

**B.** Z-80 MICROPROCESSOR INSTRUCTION SET.....

C. HARDWARE EXPANSION.....

**D.** GLOSSARY OF TERMS.....

3

39

45

51

54

58

2

### WELCOME ABOARD



This book is an all inclusive tour of the wonderful world of personal computing, with your very own Sorcerer as a tour guide. Here's a quick idea of where we are going, how we plan to get there, and some of the more interesting sights along the way.

**PACKING AND TRAVEL AIDS** start you out on the right foot as a first time traveler. You will discover how to handle your Sorcerer and should problems occur, what to do about them. We will explore the concept of a computer system and what is involved in putting one together. Finally you will want to talk to your Sorcerer, so we will explain how that is done.

**BON VOYAGE AND INDIVIDUAL TOURING** are for everyone. You will discover that your conversation with Sorcerer will be conducted by typing and reading messages displayed on a television-like screen. A detailed trip through the keybord will make you aware of the power and flexibility of your Sorcerer. Individual tour packages are introduced as useful extensions of your Sorcerer's capability.

THE EXPERIENCED TRAVELER will not want to miss Scheduled Program, Special Offering and Further Information. We will explain the language of Sorcerer's Power-On Monitor, and teach you how to create your very own symbols or character set to be displayed on the screen. You will find the hidden capability of a communications interface and printer connection for that information you need written down. A detailed description of the essential add-on components (video monitor and cassette tape drive) gives you a complete understanding of their capability and use.

**THE APPENDIX** has a wealth of information for those who like detail. A memory map of program locations shows which memory spaces are available for your use, and which ones the Sorcerer has reserved for itself. The Z80 microprocessor instructions are listed by both mnemonic and op-code. For those who want to explore far away places a list of hardware options if provided, and there is a glossary of buzz words.

As you read this manual you will discover that learning can be fun. The Sorcerer will introduce you to general purpose digital computing and provide an instant response as you experiment with its operation and programming. There's no waiting and wondering if you performed an operation correctly, Sorcerer will let you know who's in command. Let's get started!

# PACKING AND UNPACKING

# (System Hook-Up and Turn-On)



Please check the package for damage. If there is any, report this IMMEDIATELY to the shipping agent. Examine your Sorcerer carefully for concealed damage and report this IMMEDIATELY to an authorized Exidy dealer, or to Exidy Customer Service.

### **CUSTOMER SERVICE INFORMATION**

All inquiries relating to service or repair of Exidy computer products should be directed to an authorized Exidy dealer or to Customer Service, Data Products Division, 390 Java Drive, Sunnyvale, California 94086. You must have a return authorization number for all merchandise returned.

Carefully unpack the Sorcerer. Remove all packing material. Be sure to locate all cables, documentation, cartridges, etc. Save the shipping carton in case you need to transport the Sorcerer.

CHECKLIST:	Be sure you have each item.
	☐ Sorcerer Computer, 1 each
	☐ Video Display Cable, 1 each ☐ Cassette Recorder Cable, 2 each
	$\square$ Guided Tour Manual, 1 each

Place the Sorcerer on a counter, desk or other suitable surface, making sure you don't block air passage to the ventilation holes in the Sorcerer case. Plug Sorcerer into any standard, grounded electrical outlet with 120 volt, 60Hz AC power. (Note: Sorcerer can be factory set for operation of 220 volt, 50Hz AC power.)

## INSTALLATION TIPS AND SAFETY POINTS

• WHERE TO INSTALL YOUR SORCERER:

Choose a place where sunlight does not shine directly on the screen.

The set has air vents at the bottom and at the back to prevent overheating. These vents should be uncovered at all times to allow for proper ventilation.

Avoid dusty places, and do not place your Sorcerer near radiators or other heat sources. Overheating can lead to faulty performance.

Electrical appliances should always be kept away from moisture, and your Sorcerer is no exception!

- DO NOT ALLOW ANYTHING TO BE PUSHED INSIDE THE COM-PUTER THROUGH THE AIR VENTS.
- •NEVER REMOVE THE TOP COVER. REMOVAL OF THE COVER PROVIDES ACCESS TO THE ELECTRICAL COMPONENTS AND UNQUALIFIED PERSONS TOUCHING THESE MAY RECEIVE BURNS AND/OR ELECTRICAL SHOCKS.
- DO NOT SUBJECT YOUR SORCERER TO IMPACT OF ANY KIND.
- DURING A THUNDERSTORM IT IS ADVISABLE TO UNPLUG YOUR SORCERER FROM THE MAIN ELECTRICAL POWER.
- IF THE COMPUTER PRODUCES ABNORMAL SOUND OR SMELL DURING ITS OPERATION, TURN THE POWER OFF, UNPLUG THE UNIT AND CONTACT YOUR DEALER.
- •DO NOT USE ALCOHOL, BENZENE, THINNER OR CHEMICAL SOLVENT TO CLEAN THE CASE. A SOFT CLOTH DAMPENED WITH WATER IS ALL YOU NEED.

### **INSERTING A ROM PACTM**

ROM PACs are plug-in program cartridges which resemble 8-track stereo cartridges. They plug directly into the side of Sorcerer. When properly inserted, a ROM PAC automatically becomes part of Sorcerer's memory (see Appendix A). Your Exidy dealer has ROM

PACs containing languages and operating systems such as Standard BASIC, a Word Processor, and a Development PAC<sup>TM</sup> (Assembler/Editor). Insert these cartridges into the side of the Sorcerer (label side up) when the AC power is OFF.



**CAUTION:** Cartridges should be inserted or removed from Sorcerer ONLY when the AC power is off, to protect the programs.

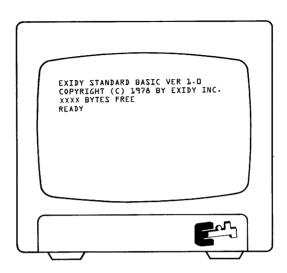
Turn the power to Sorcerer off by depressing the rocker switch at the back of the unit. Insert the ROM PAC into the side of Sorcerer (label side up). Push on the cartridge until it feels like it is in place with its mating connector. The cartridge indentations for gripping should be exposed outside Sorcerer.

### **CONNECTING THE VIDEO DISPLAY**

The video signal output at the rear video connector of Sorcerer is composite video (refer to Figure 1). The cable provided has a mating connector for both the Sorcerer output and the Exidy video display. (Note: An approved second source of video display for the Sorcerer is the Hitachi P/05M receiver monitor.)

If you do not have a video display monitor you can use a standard black and white television *if* you have an FCC approved RF modulator. The modulator connects the video output of Sorcerer directly to the antenna lead-in connection of your television set. This is cumbersome and produces a more distorted picture than a direct video display monitor.

When the video cable is attached to the Sorcerer and power is applied to both the display and the Sorcerer, a message will appear on the screen:



With a ROM PAC inserted correctly into the side of Sorcerer, you will see a ROM PAC sign-on message on the screen. This is the Standard BASIC signature.



Without a ROM PAC inserted into the side of Sorcerer, the internal Power-On Monitor program is automatically executed and the above message displayed.

If you do not see a message on the screen, proceed as follows:

- 1. Check the AC power outlet to the Sorcerer and video monitor, by plugging in a lamp and making sure it comes on.
- 2. Turn the power switch off and then on again at the back of the Sorcerer.
  - 3. Turn up the brightness control on the monitor until the screen is almost white. (Note: The brightness and contrast controls should be set to a comfortable reading intensity. *CAUTION:* Setting the intensity too high may damage the screen.)
- 4. Check the connectors on both ends of the video cable to make sure they are secure.

IF YOU STILL DO NOT SEE THE APPROPRIATE MESSAGE ON THE SCREEN, CONTACT YOUR EXIDY DEALER FOR HELP.

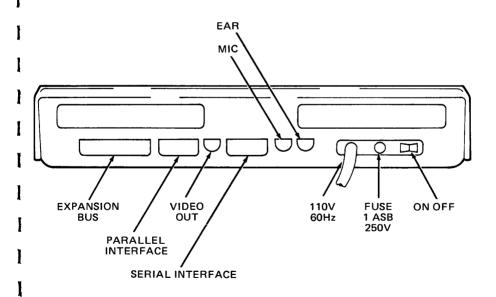


Figure 1. Connector Locations at the Back of Sorcerer

### CONNECTING THE CASSETTE RECORDER

It is not necessary to connect the cassette recorder unless you plan to load programs into Sorcerer or save programs stored in Sorcerer by recording them on a cassette tape.

There are two types of connectors at the back of Sorcerer for connection to cassette recorders (refer to Figure 1). Most of the time you will be using only one cassette recorder in manual operation, either playing a pre-recorded program into Sorcerer, or recording a customized program out. For those simple operations you will find two identical cassette recorder cables included with Sorcerer. Use them to connect the cassette recorder (MIC and EAR connectors) to the appropriately labeled MIC and EAR connectors at the rear of the Sorcerer.

If you want to conect two recorders, or have Sorcerer control your recorder's on-off switch, you can order a special data cable, Part No. DP4005. This cable attaches to the 25-pin connector labeled Serial Interface (refer to Figure 1).

It is important to adjust the cassette recorder for proper tone and volume setting. A general rule of thumb is a setting of half-on for both tone and volume. (Example: If your control knob is labeled 1 to 10, a setting of 5 would be appropriate.) Every recorder has different characteristics. The type of cassette tape used also effects the performance. Exidy data cassettes are recommended (Part No. DP3001) and are available as accessory items.

You can test the cassette tone and volume adjustments by writing and reading some test data. The following procedure uses the Sorcerer's operating system as a data source.

Depress the SHIFT LOCK key

Type: DU EOOO EOJF RETURN

(Be careful to include all the spaces.) Sorcerer will print a table of numbers and letters on the screen. Copy the body of the table on a piece of paper (don't bother with the data at the top and left side).

Now check that the two connecting cables are installed in the proper connectors of the recorder and Sorcerer. Insert a blank cassette into the recorder and rewind to the beginning of the tape. Press the RECORD and PLAY buttons on the recorder and wait five seconds.

Now type: SA TESTL EDDD EDLF RETURN

After a few seconds the prompt (>) will appear; this means your data has been recorded. Rewind the tape.

Type: LD TESTL L DDDD RETURN

Press the PLAY button on the recorder. After about half a minute, this message should appear on screen:

FOUND - TEST1 0020 E000 0000

LOADING

Shortly after, Sorcerer should print some additional information and another prompt, to tell you that the data has been loaded back into the Sorcerer.

If you cannot get the FOUND message, or if you get a tape ERROR message, rewind the tape, re-adjust the recorder's tone and volume, and try again. When you get the data loaded into the Sorcerer,

Type: DU 0000 001F RETURN

Sorcerer will print a table of numbers and letters; compare this with the data you copied on paper.

If you have a Standard BASIC ROM PAC, you can test the cassette tone and volume by writing and reading a test program. Insert the ROM PAC as described above, and follow this procedure:

Depress the SHIFT LOCK key

Type: NEW RETURN

Type: 10 PRINT "THIS IS A TEST" RETURN

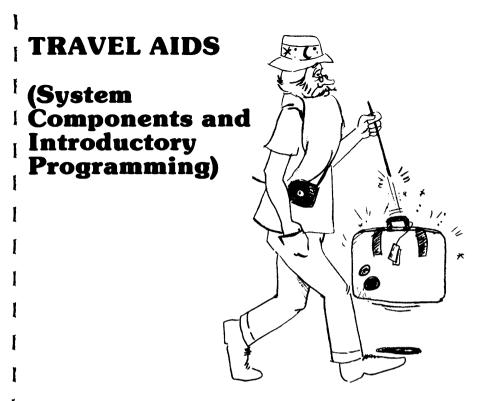
20 END RETURN

To make sure the Sorcerer accepted your test program into memory:

Type: LIST RETURN

The program should appear on the screen.

Execute the program.	
Type: RUN RETURN	•
The message THIS IS A TEST should be displayed on the screen. To save your program on a cassette tape proceed as follows:	[
Verify that the two connecting cables are installed in the proper connectors at the cassette recorder and back of Sorcerer. Insert a blank cassette into the recorder and rewind to the beginning of tape.	! [
NOTE: Most cassette recorders have automatic level control (ALC) for recording and no adjustment is necessary to record programs.	[ [
Press the RECORD and PLAY push buttons on the recorder. Wait 5 seconds.	I
Type: CSAVE TEST RETURN	I
After a few seconds the message READY will appear on the screen. This means your program has been recorded.	I
Rewind the cassette tape.	1
Type: NEW RETURN	1
Type: LIST RETURN	1
Your test program should not print out on the screen. It has been erased by the command NEW.	1
Type: CLOAD TEST RETURN	•
Press the PLAY button on the recorder and after a few seconds the message READY will appear on the screen. Stop the recorder.	1
Type: LIST RETURN	l
Your test program should have loaded into memory and should now be displayed on the screen.	( [



By now you have discovered all the components that are required to make up a computer system. The Sorcerer is the main unit with the computational power of the system, but the system isn't complete without display and reasonable program storage capability. That is the purpose of a video monitor and cassette recorder.

With the video monitor attached to Sorcerer you have a functional computer. Even without the ROM PAC<sup>TM</sup> cartridge you can perform simple operations through keyboard commands, and communicate with Sorcerer through our *Power-On Monitor*. This operating system is very limited, however, and you will want use the ROM PAC cartridges to expand the power and usefulness of Sorcerer.

SOFTWARE — Computer programs or lists of instructions that tell the computer what to do. Usually on cassette tape, permanent memory or printed in the form of a listing.

A detailed description of the Power-On Monitor program is given in the section titled SCHEDULED PROGRAM.

The ROM PAC<sup>TM</sup> cartridges are many and varied. The software or programs provided in the cartridge are what we in the computer world refer to as **systems software**. Systems software is a measure of the power, flexibility and usefulness of the computer. It is the systems software that communicates with the various input and output attachments of the Sorcerer (which could number as many as 256). The systems software also allows the user — you — to program in higher level languages.

HIGHER LEVEL LANGUAGE — A means of communication with computers at a level of understanding much like conversation with another person.

By programming in higher level language, you can instruct the machine to perform operations by use of word statements and commands, rather than cumbersome machine instructions.

### Higher Level Language Example in BASIC

Line Number	Statement
1	PRINT "Do someting for me"
2	GO TO 1

Computers perform instructions in sequence; thus, we type a line number for each operation and then the instruction we want executed. In the example, PRINT and GO TO are BASIC language statements. If you have correctly inserted a Standard BASIC ROM PAC into the Sorcerer, these commands will cause the Sorcerer to print the message within the quote marks on the video monitor and then execute the next line instruction. In this case the GO TO 1 instruction sends the Sorcerer back to printing the message again.

If you were to type the example program into Sorcerer following each line with a RETURN, you would find the computer in an endless loop.

If you want the program to pause, or the screen display to stop scrolling, press the RUN/STOP key. Release the RUN/STOP key, and the program or display will continue. (The ESC key will do the same thing.)

To stop a program or listing completely, press the CTRL and C keys simultaneously. When you stop a program this way, Sorcerer will print a message telling what line of the program it stopped at.

Before you begin writing programs, it is helpful to have some knowledge about programming in general. We all use manual programs to make everyday decisions. You might say programming is as common as getting out of bed in the morning, driving to work or planning your retirement.

Let's explore programming for a few minutes with a look at *your* internal program. You have a lot in common with the fundamentals of computing. Your microcomputer is your brain with memory and muscular/nerve system for input/output control.

### Let's Compare Your Computer with Sorcerer . . .

FUNCTIONS	EXIDY SORCERER	YOU
Central Processing Unit (CPU)	Z80	Brain
Program Storage	Memory (ROM, RAM, Cassette)	Mind
Interface	I/O Circuitry	Nerve System
Input	Typewriter Keyboard	Eyes, Ears, etc.
Output	Video Display	Mouth, Hands, etc.

As you can see, computers have been around for a long time, in one form or another; so has computer programming. A program is just a sequence of operations or instructions designed to solve a problem or perform a task.

### Let's Examine a Typical Program Sequence

**Problem:** It is getting late, you are tired, you want to go to sleep.

Solution: Go to bed.

STEP	OPERATION	1
1	Secure Household	[
2	Go To Bedroom	
3	Prepare for Bed	1
4	Set Alarm Clock	
5	Get Into Bed	
6	Close Eyes	
7	Count Sheep	I
8	Sleep	_
9	End	1

Of course you don't think of each program step as you perform it, because it's something you do every day and by now is second nature. However, when you plan to do something new or different, you give it a little thought or preparation (programming) before you do it (execution). Sometimes it's easier to determine the best direction or sequence by looking at an overall view of what you want to accomplish. In computer talk this procedure is called writing a *flow chart* of your program. Flow charts use simple geometric outlines for each step in a program as shown in Figure 2.

Flow charts help you visualize a problem and organize the best program solution. They also let you explain your program to someone without going into great detail.

The next step is to break down the operations to specific instructions which your computer understands. Your Sorcerer can communicate in many different languages, and you can change languages just by changing program cartridges. This provision is important because languages are more efficient or convenient in some applications than in others.

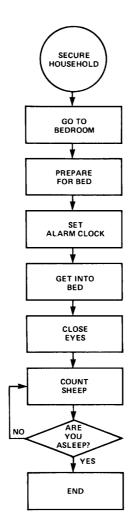


Figure 2. Program Flow Chart

You don't have to know how to program to use your Sorcerer because many programs are readily available on inexpensive tape cassettes.

BASIC is considered to be the best language for educational applications; FORTRAN (derived from the words FORmula TRANslation) is, as you might suspect, a good scientific language; COBOL is a business oriented language, and the list goes on.

Application programs are the functional programs of computing. They are, as the name implies, programs that are applied to perform tasks. Many application programs have been written for game playing, book-keeping, education, etc. and Exidy provides these programs on cassette tape to keep the expense of your program library in line with your budget for personal computing. Cassettes are much less expensive than program cartridges and many different programs may be recorded on one cassette.

1

You will even find many application programs published in personal computing magazines. These can be typed directly into Sorcerer for recording on cassette, if written in Standard BASIC or the program language that corresponds with the ROM PAC cartridge inserted in the Sorcerer.

While you are learning how to program, it is always helpful to look at a program written by someone else, for examples in technique. You do this in Standard BASIC by typing the LIST command after putting an application program in memory. This is discussed in detail in the BASIC Tour manual which accompanies the ROM PAC cartridge.

There isn't enough space in this book to teach you to program in all the languages; there are too many of them, and new ones are being developed all the time. However, a complete instruction manual is included with each ROM PAC.

### **BON VOYAGE**

### (Operation and Keyboard Description)



Before you embark on your tour, the Sorcerer will provide escort service so that you become familiar and comfortable with your new surroundings.

Test the keyboard by typing in your name. If you type a wrong character, you may erase it by depressing SHIFT RUB. Then proceed as you were. Don't worry about making mistakes — you already know how to erase them. Your Sorcerer will also be looking for mistakes. Since your name is not a legal command, Sorcerer would display an error message if you were to press RETURN following your name.

Remember, you can't damage the Sorcerer with keyboard operations . . . you may confuse Sorcerer, but you can always re-establish communications by simultaneously pressing two RESET keys.

You can order Sorcerer to automatically solve a problem hundreds or even thousands of times, with new information each time. Since the computer can run through hundreds of program steps in a second, it's ideal for long and repetitious problem solving.

# You must understand the keyboard if you are to have any meaningful conversations with Sorcerer.

The keyboard is similar to a standard typewriter keyboard, with a few additions. The letters and control keys are virtually in the same positions, but for your convenience and speed in numerical computations, a numeric pad is provided (see Figure 3).



Figure 3. The Keyboard (Characters without Shifting)

Since your Sorcerer has both upper and lower case characters, you will find its operation identical to a typewriter. And, since many programs are written only in upper case characters, there is a shift lock key for your convenience.

If you press a key without shifting, you will get a lower case character.



Figure 4. Characters with Shift Lock Key Depressed

When you simultaneously press the SHIFT LOCK key and a letter, you get the upper case representation or notation as shown in Figure 4.



Figure 5. Characters with Graphic Key Depressed

When you simultaneously press the **GRAPHIC** key and a letter you get the graphic symbol shown in Figure 5.

The graphic symbols shown on the key tops are a special defined set for Sorcerer. They are used to draw pictures, lines and bar charts on the screen. They can be used just like any other letter or digit on the keyboard.

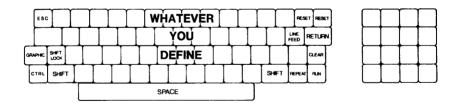


Figure 6. Characters with Shift and Graphic Keys Depressed

Pressing SHIFT and the GRAPHIC key simultaneously with any letter will give you one of your own characters. (See the SPECIAL OFFER-ING chapter for detailed instructions on how to program your own character set.)

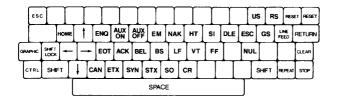




Figure 7. Characters with Control Key Depressed

Figure 7 shows how you can perform the standard TTY and computer terminal functions, by pressing the CTRL key simultaneously with the designated keys. These functions correspond to ASCII characters 0 through 31. If you are new to personal computing, you can ignore these completely.

Figure 7 also shows which keys control the video monitor cursor. With the CTRL key depressed, either of the HOME keys will put the cursor in home position (upper left corner of the screen), while an arrow key will move the cursor 1 space in the direction of the arrow.

### **EXPERIENCED TRAVELER**

# (Scheduled Program)



### ALL SORCERERS ARE CREATED EQUAL.

The basic (meaning fundamental) intelligence of your Sorcerer is a program called the **Power-On Monitor**. Should you ever get lost, confused, even bewildered, an understanding of the Power-On Monitor will prove to be a useful tool in regaining your composure and discovering where your problem lies.

The Monitor program is buried inside the Sorcerer and has nothing to do with program cartridges. It does, however, look to see if a program cartridge is installed in the unit and if so, it will pass control to the cartridge. When a program cartridge isn't installed, the Power-On Monitor program will take control and print >.

Hint . . . if you have a program cartridge plugged into Sorcerer and a > is displayed on the screen, you don't have the cartridge inserted properly.

The greater than [ > ] character is referred to as a **prompt**; it lets you know that Sorcerer is awaiting a command from you. The underscore character [\_\_] is called a **cursor** and tells you where the next character will appear on the screen.

The Monitor program can be called upon at any time. Not everyone will want to escape into the Monitor because it isn't nearly as fun as BASIC or other higher-level languages. Its main purpose in life is to diagnose hardware and software programs. For those of you who want that challenge, here is a list of Monitor commands.

### **MONITOR COMMANDS**

The commands are listed in the left column; only the first two letters are necessary. The *parameters* of each command are listed to the right; parameters enclosed in parentheses are optional. You must use spaces or other delimiters between the command and its parameters, and between parameters. Follow the last parameter with a RETURN.

<u>DU</u> MP	XXXX addr	(XXXX) addr
	addi	addi

Displays memory on the screen. If you give one address, you get the contents of that address; if you give two addresses, you get the contents of all addresses from the first to the second.

### Examples:

DU 0100	Displays the contents of	f
	address 0100.	

DU FE00 FE07	Displays the contents of all
	address from FE00 to FE07.

## ENTER XXXX addr

Lets you enter hexadecimal numbers into the memory address. When you hit the carriage return, Sorcerer advances to the next address; when you hit carriage return alone, you just get the current address. A / (slash), followed by the carriage return, gets you out of ENTER.

}				
}		Examples:		
)		Entering data one address at a time:		
}		EN FE00	The command to enter data, starting at FE00.	
]		FE00: FF	Sorcerer prints the addresses and colons;	
, ]		FE01: 00	You must enter the numbers (following each with a RETURN .	
1		FE02: FF		
,		FE03: 00		
1		FE04: /	The slash terminates the EN command.	
1		Entering a block of data:		
}		EN FE04	The command to enter data, starting at FE04.	
}		FE04: FF 00 FF 00	Sorcerer prints the address and colon; you must enter the numbers, separated by delimiters (in this case, spaces).	
}		FE08: /	Sorcerer reports the next available address; the slash terminates the EN command.	
}	<u>SA</u> VE	NAMEX XXXX XXX name addr addr	• •	
,			from the first address to the ape unit specified (or on unit	
,		#1, if you don't specify).	The header on the tape will must start with a letter), the	
J			size, file type, and GO ad-	
]		25		

### Example:

SA TEST1 0100 0240 2 Saves the program in addreses 0100 to 0240 on tape unit #2, under the name TEST1.

### <u>LO</u>AD(G) (NAMEX) (X) (XXXX) name unit addr

Loads the file named NAMEX into memory, from the tape unit specified (or unit #1, if you don't specify). Starts loading at the given address if specified; otherwise at the address in the file header on the tape. If you specify an address you must also specify the unit. If you don't give a name, then the first file will be loaded. If you include the G after LO, Sorcerer will run the program starting at the GO address in the file header, immediately after loading. This command will only load files saved in the Monitor. A file saved in Standard BASIC must be loaded with the command CLOAD; a file saved in the Monitor can be loaded with CLOAD.

### Examples:

LOG

LO TEST1	Loads the program TEST1 from tape unit #1, starting at the address in the file header.
LO TEST1 2	Loads the program TEST1 from tape unit #2, starting at the address in the file header.
LO TEST1 1 0100	Loads the program TEST1 from tape unit #1 starting at address 0100.

Loads the first program on tape unit #1 starting at the address in the file header, and runs the program.

ł		·				
' 	LOG 2 0100	Loads and runs the first program on tape unit #2, starting at address 0100; then jumps to the GO address in the file header.				
 	LOG TEST1	Loads and runs the program TEST1 from tape unit #1, starting at the address on the file header.				
<u>FI</u> LES	(X) unit					
 	specified unit. CTRL C : you to the Monitor. This c	ts all cassette files and header information from the crified unit. CIRL C stops the listing and returns a to the Monitor. This command will list BASIC proms stored with CSAVE, but will <b>not</b> list arrays ed with CSAVE * .				
<u>GO</u> 	XXXX addr Calls a program at the given	ven address.				
MOVE	XXXX XXXX (S)XX addr1 addr2 block/a					
	Copies the contents of a block of successive memory addresses into another block of addresses. Use this command two ways:					
	1. If you include the S, then the following number tells Sorcerer how many addresses to move. The contents of address 1 go into address 2, and the addresses following address 1 are copied after address 2.					
	2. If you omit the S, then Sorcerer moves the block of memory starting at address 1 and ending at address 2. The contents of address 1 go into address 3, and the following addresses are copied after address 3.					

### Examples:

MO FC00 FE00 S0010

This copies the 16 addresses (10 Hex) starting at FC00, into the 16 addresses starting at FE00.

MO FC00 FC0F FE00

This copies the 16 addresses from FC00 to FC0F into the 16 addresses starting at FE00.

### TEST

XXXX XXXX (C) addr1 addr2

Tests each bit of RAM from address 1 to address 2 (8 bits per word). A blinking \* appears while the test is running. No message if memory is good; otherwise prints BAD or OK for each bit. With C, tests continuously, giving a PASS COMPLETED message at each pass. Use this command only for testing RAM; all ROM addresses will be reported BAD, no matter what their condition.

### NOTE

The bit test (command TE) will not give reliable results on the area of RAM occupied by the Monitor stack. If you wish to test this portion of RAM, first relocate the Monitor stack, as described under that heading. For the addresses of the stack, see Figure 9, Appendix A.

You must also take special precautions when testing screen RAM and video scratch RAM (addresses F000 to F7FF. For details, refer to the Sorcerer Technical Manual.

### **PROMPT**

=X

Changes the Monitor prompt from > to whatever you specify. For example, PR = # changes the prompt to #.

### **CREAT**

Creates a batch tape on tape unit 1. Enter your batch commands a line at a time after the \* prompts, and end each line with a carriage return. The tape will start, record your line, and stop. To reenter the Monitor, hit the carriage return alone.

, <u>Ц</u> ЅТ 1	Lists all the commands on your batch tape.					
<u>BA</u> TCH   	Tells Sorcerer to read the batch tape on tape unit 1 and execute all the commands on that tape. You can stop the routine by typing CTRL C; Sorcerer will stop automatically if it reads an invalid command, or if any error occurs.					
l <u>ov</u> er I	This is the batch command which terminates the batch mode and returns you to Monitor. This is similar to the END statement in Standard BASIC.					
SET	S = XX T = X F = XX X = XXXX	Sets tape rate to 0 = 1200 baud (default); 1 = 300 baud.  X Sets file type in tape header to ASCII value of XX. If D8 is set, then file is non-auto-execute.				
	O = V = P	Sets current output port to	PARLOT (parallel output) CENTRONICS PRINTER			
	= S = XXXX I = K = P	Sets current input port to	DRIVER OUTAPE (tape output) ADDRESS KEYBRD PARLIN (parallel input)			
	= S = XXXX	90	INTAPE (tape input) ADDRESS			
		29				

### **NOTE**

There are 256 file types (00 through FF). D8 is non-auto-execute. All others are arbitarily user assignable. However, BASIC will only load a file of type BX (i.e., most significant digit is B).

### Examples:

SE S = FF Sets the display delay to FF

SE F = D8 Sets file type to non-auto-

execute.

SEO = L Sets output to Centronics

Printer.

SE O = 1000 If you have loaded an output driver routine at ad-

dress 1000, this command sets the Sorcerer's output

to that routine.

<u>PP</u> (X)

Jumps to ROM PAC. If no parameter, then warm start, otherwise cold.

**NOTE:** Each address is a four digit hexadecimal (base 16) number, from 0000 to FFFF (0 to 65535 in decimal notation). The content of each address is a two digit hexadecimal number, from 00 to FF (0 to 255 decimal).

Until the list of Monitor commands came along your tour of personal computing was pretty civilized. In this chapter you're about to have a close encounter with the stuff the computer world is famous for . . . abbreviations, mneumonics, hexadecimal notation and never enough explanation in the instructions. Never fear . . . Your Sorcerer will help you through all this because if you don't understand an instruction *experiment* until you get a resonable response.

Let's go back and look at that list again. DU is an abbreviation for "dump memory," and in fact that is exactly what happens if you type DU followed by an address that you want to display, followed by a carriage return. The problem now is to understand this memory address

notation. Each digit of the four shown is a hexadecimal (base 16) digit. Hexadecimal is a number system often used by computers. It uses digits 0 through 9 and the letters A through F. The letters are used because it is necessary to represent 16 different values with a single digit for each value; the letters A through F represent the number values 10 through 15.

Using letters in counting may appear awkward until you become familiar with the system. Hexadecimal address 1E equals the decimal address 30.

DECIMAL		HEXADECIMAL	DECIMAL		HEXADECIMAL
0	=	0	9	=	9
1	=	1	10	=	Α
2	=	2	11	=	В
3	=	3	12	=	С
4	=	4	13	=	D
5	=	5	14	=	E
6	=	6	15	=	F
7	=	7	16	=	10
8	=	8	17	=	11
	0 1 2 3 4 5	0 = 1 = 2 = 3 = 4 = 5 = =	0 = 0 1 = 1 2 = 2 3 = 3 4 = 4 5 = 5	0       =       0       9         1       =       1       10         2       =       2       11         3       =       3       12         4       =       4       13         5       =       5       14         6       =       6       15         7       =       7       16	0       =       0       9       =         1       =       1       10       =         2       =       2       11       =         3       =       3       12       =         4       =       4       13       =         5       =       5       14       =         6       =       6       15       =         7       =       7       16       =

When conversation turns to talk of computer address and instructions you will always find yourself with foreign number systems. Most likely binary (base 2), octal (base 8), hexadecimal (base 16) and not often enough, decimal. For this reason you will find conversion tables in many computer publications. Since IBM is partial to hexadecimal you will find our reference to address and instructions in hexadecimal also. Computer instructions in hexadecimal are also referred to as *machine language* code which is described in great detail in the manual that accompanies the development program cartridge.

### **MONITOR NOTES**

- 1. Monitor program resides from E000 HEX to EFFF HEX.
- 2. Video resides from F000 HEX to FFFF HEX.

### 3. Power Up —

The Monitor searches and tests memory from  $0000\,\mathrm{HEX}$  to the end of memory. It then places its stack approximately 128 bytes below the top of RAM. If a ROM PAC is inserted, the monitor will then transfer to it. Otherwise, it will then print:

EXIDY STANDARD MONITOR

VERSION 1-0 COPYRIGHT (C) 1978 BY EXIDY INC-

THE TOP OF RAM IS XXXX HEX-STACK BEGINS FROM XXXX HEX-

4. Entering the Monitor —

The Monitor has three (3) entry points:

COLD WARM USER

- is always entered from power on. The RAM is searched and the top is found. The video is initialized and default parameters are set up.
- E003-WARM- establishes a stack from information from COLD or USER and re-enters the Monitor.
- E006 USER uses the HL register pair as the top of RAM and then jumps to WARM.
- 5. I/O Entry Points —

The Monitor has nine (9) I/O entry points:

- gets character from current input device to the A register; if no character, then sets the A register to zero. If character is present, Z flag is reset; otherwise, Z flag is set.
- EOOC SEND outputs the A register to current output device.
- E00F INTAPE inputs to the A register from tape.
- E012 OUTAPE outputs the A register to tape.

E015 — QUICKCK — does a quick check for CTRL C, ESCAPE or RUN/STOP. Returns with Z set if none.

E018 — KEYBRD — inputs to the A register from the keyboard, if

inputs to the A register from the keyboard, if a key was pressed. Z flag is reset or set, depending on whether a key was pressed.

E01B — VIDEO —

E9 F Ø

Outputs the A register to screen

CTRL W = cursor up

CTRL A = cursor left

CTRL S = cursor right

CTRL S = cursor right

CTRL Z = cursor down

CTRL H = character delete

CLEAR

Or

form feed

CTRL Q = cursor home

### NOTE

The form feed also resets the standard graphics to their normal values (the symbols shown on the key caps).

inputs to the A register from parallel input port.
outputs the A register to a parallel output

6. Delimiters —
The Monitor will use any of the following to delimit or to space between Monitor parameters on a line:

SPACE!"#\$%&'()\*+,-.

port.

7. Error Messages —

E01E — PARLIN —

E021 — PARLOT —

E776

ニチェド

The Monitor has three error messages:

INVALID COMMAND

INVALID PARAMETER TAPE CRC ERROR The first two are self explanatory; these are syntax errors and often result from incorrect use of delimiters. A tape error message usually indicates incorrect tone or volume settings of the tape recorder, but may also indicate a malfunction in the Sorcerer's tape interface or in the tape cartridge itself.

#### THE MONITOR BATCH SYSTEM

The Monitor batch system lets the Sorcerer automatically run a sequence of files. The four batch commands are explained above (see Power-On Monitor Command Set).

A **batch tape** is a sequence of user created Monitor commands, recorded on a tape cassette. Use the Monitor command CR to create this tape, and the command LI to verify its contents. Use the command BA to automatically execute the commands on the batch tape, and use the command OV as the last command on the tape, to return control to the Monitor. Note that the monitor does not have any commands which would allow you to edit a batch tape. If you make a mistake while creating your tape, you will have to go back and start over.

### Example:

Here is a sample batch routine. The following commands are on a batch tape ready to be played on tape unit #1. Tape unit #1 also contains the two programs PROG1 and PROG3. Tape unit #2 contains a blank tape, for recording the file FILE1.

LOG PROG1 1 PROG1 is here

LO PROG3 1 2000 PROG3 is here

SEO = L

GO 2000

SEF = AA

SA FILE1 0000 1000 2

OV

This batch routine loads the program PROG1 from tape unit #1 an executes it. Next, it loads the program PROG3 from tape unit #1 starting at address 2000. It then routes the output to a Centronics printer and executes PROG3. Then it sets the tape file type to AA and save the memory from addresses 0000 to 1000 on tape unit #2, under the name FILE1. Finally, it returns control to the Monitor.	rt- er es	
Any error will end the batch mode and return you to the Monitor. You can also return to the Monitor by hitting CTRL C, ESC, or RUN/STOP	ou D.	
Only tape unit #1 can be used for creating or executing a batch tap however, you can put a sequence of programs on tape unit #2 and ruthem with batch tape LOG commands. To use both tape units, you need a Serial Data Cable (Exidy Part No. DP4005); this cable also provide automatic control of the tape unit motors.	un ed	
We strongly recommend you use the Serial Data Cable — without automatic motor control, your batch tape may run past a comman before the preceding command has been executed. If you do not us the data cable, you can avoid this problem in two ways:	nd	
First, you can put plenty of blank tape between commands on the batch tape. When you enter each command (under the CR command let the tape run awhile before hitting RETURN).		
Second, you can start and stop the tape unit manually (although this defeats the purpose of the batch system). The Sorcerer prints each command on the screen after reading it from the batch tape; this is the time to stop the recorder. After executing the command, Sorcerer prints a prompt; you can then turn on the recorder.		
The Monitor puts your commands onto a batch tape in this format:		
One hundred 0s and a 1 a command in ASCII one byte CRC (Cycl Redundancy Check	lic k)	

As each character is written on (or read from) the batch tape, Sorcerer performs a CRC routine to update the CRC byte. The final value of this byte is then written on the tape (under the CR command) or compared with the value already on the tape (under the BA command).

#### **RELOCATING THE MONITOR STACK**

**The stack** is a term which loosely denotes three separate areas of memory in the Sorcerer:

- The Monitor RAM (temporary scratchpad storage for the Monitor)
- The Monitor stack proper (temporary storage for Z80 registers and return locations of CALLs)
- The stack pointer (a two byte register in the CPU)

To **relocate the stack** means to simultaneously move the Monitor RAM and stack proper, and reset the stack pointer.

At power-on, Sorcerer searches RAM for the top RAM address. The 112 addresses (70 hexadecimal) from the top downward are used as the Monitor RAM; the next 64 addresses approximately (40 hexadecimal) are the Monitor stack proper.

#### Example:

In the 8K Sorcerer at power-on, the top of RAM is 1FFF and the stack starts at 1F90. This means that addresses 1F91 to 1FFF are Monitor RAM storage, and 1F90 to approximately 1F50 are the Monitor stack proper.

If the stack is disturbed, the system may crash. This usually happens when a tape file or user program overwrites the stack. You can recover from the crash by hitting the RESET keys, but you will still lose the contents of all RAM. To prevent a crash, you must relocate the stack to an area of RAM which won't be used by your program or tape file. This area must contain at least 176 bytes (B0 hexadecimal) — 112 bytes for Monitor RAM storage, and 64 bytes for the Monitor stack proper.

You must also relocate the stack before using the Monitor RAM test (command TE) on the area of RAM occupied by the stack.

To relocate the stack, first, choose a suitable address XXYY for the top of the Monitor RAM; here, XX and YY are the high order and low order bytes of the address, respectively. Second, use the Monitor EN command to put these Z80 instructions into the addresses 0000 to 0005:

21 YY XX C3 06 E0

This Z80 program loads the address XXYY into the HL register pair and then jumps to the Monitor USER entry point. Finally, give the command GO 0000.

Example:

To move the stack so that the top of RAM is at 0750:

You type: EN 0000

Sorcerer replies: 0000:\_\_

You type: 21 50 07 C3 06 E0 /
Then type: GO 0000

Then type. GO 0000

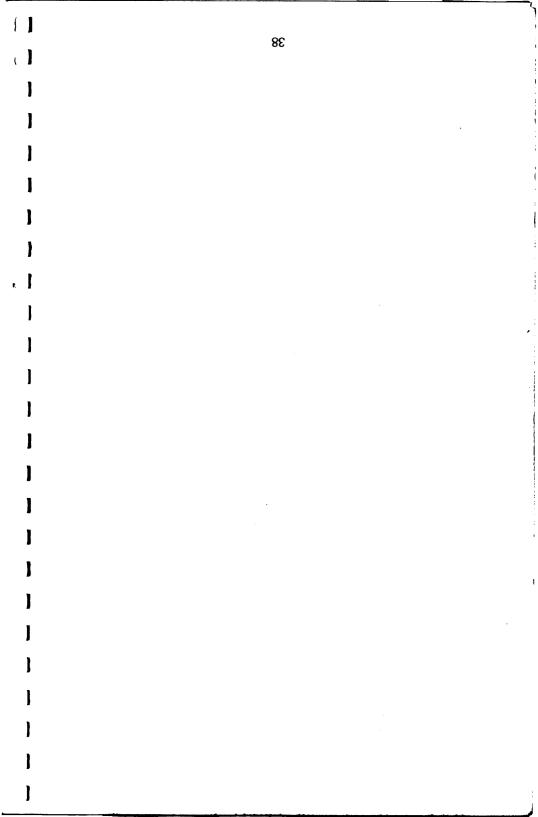
Sorcerer moves the stack and prints this message:

EXIDY STANDARD MONITOR

VERSION 1.0

COPYRIGHT (C) 1978 BY EXIDY INC.

THE TOP OF RAM IS 0750 HEX. STACK BEGINS FROM 0LEL HEX.



# SPECIAL OFFERING

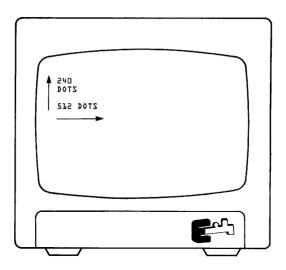
# (User-Defined Graphics)



A picture is worth a thousand words and you're about to discover how to create your own pictures with the Sorcerer graphics. In the BON VOYAGE chapter you learned about the standard keyboard graphic characters, which you can type directly onto the screen.

Let's look at the graphics more closely. Your Sorcerer has a graphic resolution of  $512 \times 240$ ; that is to say, there are 512 horizontal positions and 240 vertical positions for dots to appear on the screen. To print a character on the screen, Sorcerer selects an  $8 \times 8$  square of these positions and puts dots in just the places needed to form the character.

Since each character occupies an  $8 \times 8$  space, and the screen is  $512 \times 240$ , it follows that you can get 64 characters on a line, and 30 lines on the screen. The total number of characters on the screen at any time (including blank spaces) is 1,920.



Now the question is who gets to say what's on the screen? Using the SHIFT and GRAPHIC keys in combination with the keyboard you have access to 256 different characters. It's only fair that you be able to define as many characters as the Sorcerer so we have a compromise. 128 fixed characters (Standard ASCII) and 128 programmable.

#### THE COMPROMISE IS THIS:

## SORCERER

VS.

YOU

Since the keyboard needed to be labeled and most of the people in the world relate to typewriters, Sorcerer selected the upper and lower case alphanumeric character set and you can't change those, even if you don't speak Sorcerer's language. One-hundred twenty-eight characters you can't change.

Since there was room on the keytops, Sorcerer has assigned 64 of the 128 characters reserved for graphics. But, in this case you can change those if you want. You, too, have control of 128 characters and if you don't like Sorcerer's selection feel free to change them at will, even into your own language if you don't like Sorcerer's; after all, they don't have to be graphic symbols.

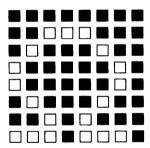
### **To Create Graphic Characters**

Now that you know what you can do, it's only fair that you know how. Each character is stored in eight successive memory addresses in the memory block FE00 to FEFF (hexadecimal). For example, the first key (the key on the main keyboard) uses FE00 to FE07. So first you have to figure out which eight addresses go with the key in which you want to store your new character.

Your character will be represented as an 8 x 8 array of dots and blanks, so get out a pencil and paper, and work out the representation. Quarter-inch grid graph paper is useful here. Each eight-place row of this array will fit in one of the eight addresses of your character's key; the top line goes into the first address, the next line goes into the second, and so on.

# Example:

One way to represent the Greek letter omega ( $\Omega$ ) is:



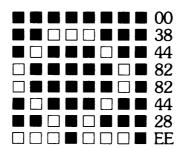
Now take each line of your array, and change it into a two-character code in this way: First split the eight place line in two. Then convert each of these two four-place lines into code with the following table:

#### Four-Place Line Code

=	0	=	8
=	1	=	9
=	2	=	Α
=	3	=	В
=	4	=	С
=	5	=	D
=	6	=	Ε
=	7	=	F

Examples: The eight-place line:

The eight lines of our letter omega are:



What you have just done is to consider each eight-place line of your character as an eight-digit binary number (white dots are 1s and blanks are 0s), and to re-write that number in hexadecimal notation. So you now have a two digit hexadecimal number for each of the eight lines of your new character; you must now get these numbers into the eight memory locations that belong to your character's key.

Get into the Power-On Monitor. Then give the Monitor command EN XXXX where XXXX is the first memory address of the desired key. For example, the first address of the key on the main keyboard is FE08, so the proper command for that key is EN FE08. Sorcerer will respond with the address you gave it, followed by a colon.

Type in the two-digit hexadecimal code of your character's first line, and hit RETURN. Sorcerer will reply with the next address in memory, and wait for more data. Type in the hex-code for your character's second line, and repeat this process until you have entered all eight lines of your character into memory. Sorcerer will then give you the address of the first memory location for the next key on the keyboard. Type in a slash (/) and hit RETURN.

Your new character is now stored in the Sorcerer. Just press and SHIFT and your character's key, and your new graphic will appear on the screen.

**Note:** Your special graphic characters normally live in memory addresses FE00 to FFFF. Addresses FC00 to FDFF contain the standard

graphic characters shown in Figure 5 on page 21; these are the characters you get using the GRAPHIC key, but not the SHIFT. You can put your own characters into these addresses if you wish; the procedure is the same as above.

When you turn off Sorcerer's power, or hit RESET all your user-defined characters will be lost. To save them for future use, pass to the Power-On Monitor and save the contents of the character's memory addresses on cassette, using the Monitor command SA. You can load the characters back into memory from the tape, using the Monitor command LO.

Figure 8 shows the memory addresses and ASCII numbers of userdefined characters. The four digit hexadecimal number at the top of each key is the first of the eight memory addresses which store the character. The two hexadecimal numbers at the bottom of the key are the ASCII numbers of the standard graphic and user-defined graphics of that key.

### Example:

The skey on the main keyboard controls the user-defined graphic stored in addresses FED8 to FEDF. The ASCII number of that user defined graphic is DB. The ASCII number of the skey's standard graphic is 9B.

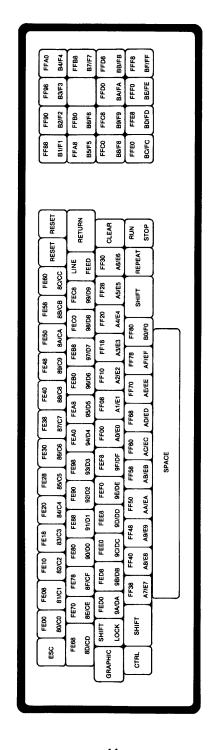


Figure 8. User Graphic Storage Addresses and ASCII numbers

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# FURTHER INFORMATION

| (Serial/Parallel | Ports, Cassette | I/O, Video, | S-100 Bus)



Until now we have described the Sorcerer as a complete computer system having the fundamental ingredients of central processor, memory, keyboard input, video display output, and cassette tape for program storage and playback. The actual hook-up or installation of the computer system was relatively easy and with A.C. power applied to all the units, the operation was automatic with a small degree of prompting.

As you have seen, life with the Sorcerer is more interesting when you write your own programs (or customize someone else's). You will find that customizing your system's hardware can be just as much a challenge. Therefore, we should prepare you for further exploration in the hardware domain with an explanation of the built-in electronic interfaces.

### **PLUG-IN CARTRIDGE**

The ROM PAC Cartridge, even though it can be plugged in and out, is just an extension of the computer's main memory; therefore, it does not qualify as an I/O interface and we shall say no more about it.

#### **KEYBOARD**

Your keyboard is definitely an I/O device. Specifically, it is an input device. However, it is totally dedicated to your communication with the Sorcerer and you cannot customize it, so enough said.

#### **VIDEO DISPLAY**

The video is also dedicated to your communication with Sorcerer but a certain degree of flexibility is desirable. A standard composite video signal is provided in the video connector at the back of Sorcerer for attachment to the display unit of your choice. The range of video display units varies from 5" to 23" with many popular sizes in between. Your choice is a toss-up between character size displayed and your eyesight.

#### **CASSETTE TAPE**

The cassette interface is really your first encounter with programmable I/O. You can connect a second tape drive and implement the computer-controlled motor on/off. As you recall, the simple cassette operation just entailed plugging the two cables provided to the MIC and EAR phono jacks for cassette recorder #1 and manually pushing the play and record buttons. If automation is desired, Table 3 provides the programming data and command instructions and Table 4 provides the electrical connection information.

#### LINE PRINTER

Sorcerer has a Parallel Data Port, which we have not yet investigated. This data port is not essential to the normal operation of your Sorcerer, but you will need to know about it if you want to print your output on paper. When you add a hardcopy printer to your system, you will attach it to the left hand 25-pin connector at the rear of the Sorcerer; Table 2 provides the programming data and command instructions and Table 5 provides the electrical signal information.

# REMOTE TERMINALS

The Sorcerer can be used as a smart terminal in applications where communication to another computer large or small is desired. This feat is accomplished with the serial I/O interface provided by the right hand 25-pin connector at the rear of Sorcerer. Attaching a data cable to this connector and either a modern or acoustic coupler enables you to transmit and receive information via telephone lines. The serial I/O is RS232 level compatible with data transfer rates of either 300 or 1200

baud. The baud rate is software selectable and the programming data and command instructions are provided in Table 1. The electrical signal information is described in Table 4.

### **EXPANSION**

The slot at the rear of Sorcerer allows connection of the Exidy Expansion Unit. You will notice a 50 pin printed circuit edge connection on the main P.C. board. This is mated to a 50 pin edge connector and flat cable for connection to the S-100 bus translation card in the Exidy Expansion Unit.

**Table 1. Serial Data Port Programming Assignment** 

Ì	I/O Port #	Bit #	Input	Output
I I I	FC	0 1 2 3 4 5 6 7	input data bits	output data bits
]	FD	0	transmit buffer empty	bits per character (NB1)
•		1	data available	bits per character (NB2)
J		2	over-run	number of stop bits
l		3	framing error	parity select
_		4	parity error	no parity

Table 2. Parallel Data Port Programming Assignment

I/O Port #	Bit #	Input	Output
FF	0 1 2 3	input data bits	output data bits

# Table 3. Cassette Recorder Programming Format

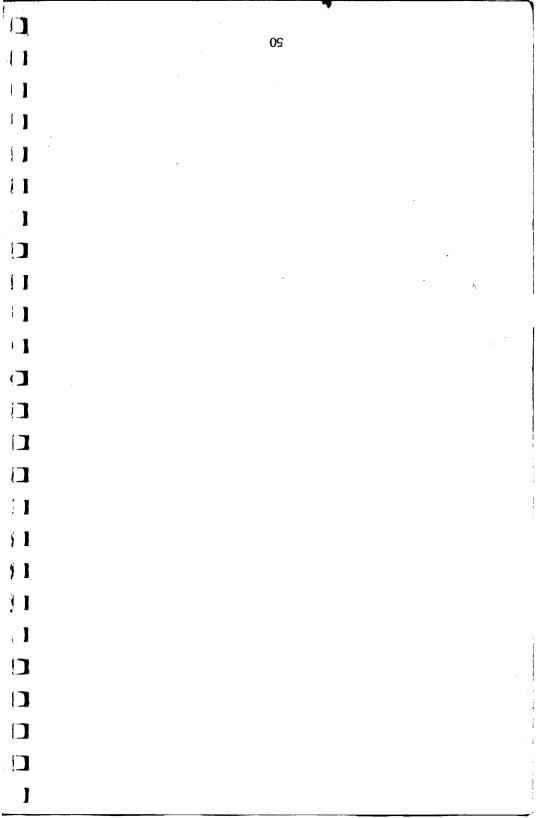
I/O Port #	Bit #	Input	Output
FC	0 to 7	input data bits	output data bits
FD	0	transmit buffer empty	
	1	data available	
FE	4		motor control #1
	4 5 6		motor control #2
	6		baud rate $(0 = 300 \text{ baud}, 1 = 1200 \text{ baud})$
	7		interface (0 = cassette, 1 = RS232)

Table 4. Serial Interface (Right Hand 25 Pin Connector)

Pin #	Signal	Pin #	Signal
1	Shield 1	13	Motor #2+
2	RS232 OUT	14	Shield 2
2 3	RS232 IN	15	Mike 1
	Ground	16	Mike 2
4 5	Aux 1	17	Ground
6)		18	Aux 2
7 }	Ground	19	Ground
8)		20	Ear 1
9´	+ 12 volts	21	Ear 2
10	Unused	22	Unused
11	RS232 IN	23	RS232 OUT
12	Motor #1+	24	Motor #1—
		25	Motor #2—

Table 5. Parallel Interface (Left Hand 25 Pin Connector)

Pin #	Signal	Pin #	Signal
1	Ground	13	Input Bit 6
2	Output data accepted	14	Unused
3	Output data available	15	+5 volts
4	Output bit 7	16	Output bit 0
4 5	Output bit 6	17	Output bit 1
6	Output bit 5	18	Output bit 2
7	Output bit 4	19	Output bit 3
8	Ground	20	+5 volts
9	Input data available	21	Input data accepted
10	Input bit 0	22	Input bit 1
11	Input bit 2	23	Input bit 3
12	Input bit 4	24	Input bit 5
	1	25	Input bit 7



#### **APPENDIX A**

#### **SORCERER'S MEMORY MAP**

The Sorcerer will recognize up to 65536 different memory locations. These locations are given numbers from 0000 to FFFF hexadecimal (0 to 65535 decimal). Each memory location will contain one of the 256 hexadecimal numbers 00 to FF (0 to 255 decimal). If the Z80 CPU is instructed to read an address that is **not** connected to any RAM or ROM, it will usually assume the data is FF.

Figure 9 shows the functions served by each block of addresses. Column A shows the 8K Sorcerer with an 8K ROM PAC (such as Standard BASIC) inserted; the diagram is not to scale. Column B is the same as column A, but redrawn to approximate scale.

Columns C and D show alternative configurations. The lower portions show the addresses of internal RAM in the 16K and 32K Sorcerers, while the upper portions show the addresses of the 4K and 16K ROM PACs.

The unassigned addresses between the top of internal RAM and the bottom of the ROM PAC can be given to the S-100 Expansion Unit. If the ROM PAC is unplugged, its address space is also available to the S-100 unit.

The Monitor stack, consisting of the Monitor RAM and the stack proper, is shown at the top of the internal RAM, the position it usually takes at power-on or reset (see Relocating the Monitor Stack, for details). But, note that if a block of expansion memory is assigned immediately above internal RAM, the stack will go to the top of that block

# Example:

In the 16K Sorcerer at power-on or reset, the Monitor stack occupies addresses 3F50 to 3FFF. If 4K of memory (1000 Hex. addresses) is added to the Expansion Unit and assigned addresses 4000 through 4FFF, the Monitor stack will occupy 4F50 through 4FFF at power on. However, if the new memory is assigned to 5000 through 5FFF, the stack will remain 3F50 to 3FFF.

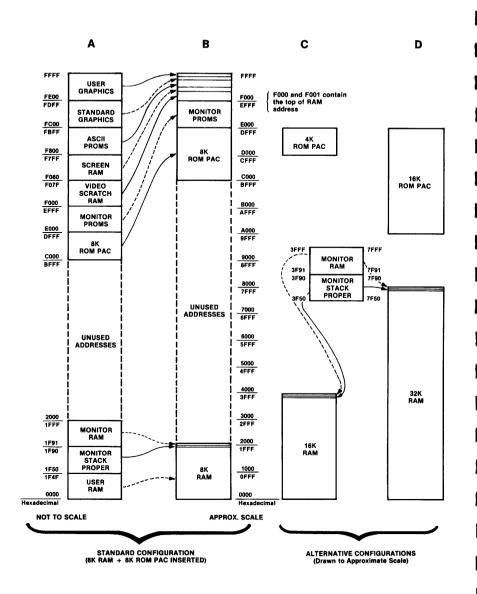


Figure 9. Sorcerer Memory Map

Every character (alphanumeric, graphic, or user-defined) is printed on the screen as an 8 x 8 array of dots. Each dot can be either ON (a white spot on the screen) or OFF (a dark spot). The exact shape of the character depends on which dots are turned on, and which are turned off.

١

1

I

Ì

I

1

Each row of the character is stored in the Sorcerer's character generator, in a separate memory address, with each dot of the row stored as one bit of that address. A 0 bit means OFF, and a 1 bit means ON.

To write a character onto the Sorcerer's screen, put its ASCII number into the screen RAM. The exact address you use will determine where the character will appear on the screen. The ASCII number directs the Sorcerer to eight successive addresses in the RAM or ROM character generator. These addresses store the eight lines of the character's dot matrix.

Each text line on screen is 64 characters long (40 hexadecimal), and there are 30 lines in all (1E hexadecimal). The first address in screen RAM (F080) is for the upper left corner of the screen. The next address (F081) is for the second place on the first text line, and so on to the end of the line (F0BF). The pattern continues with the first place on the second line (F0C0) and so forth. The last address (F7FF) is for the lower right corner of the screen.

# **APPENDIX B**

# **Z80 MICROPROCESSOR INSTRUCTION SET**

Z80-CPU
INSTRUCTIONS
SORTED BY
OP-CODE

SORTED BY OP-CODE			
OBJ CODE	SOURCE STATEMENT		
00 018405 02	NOP LD BC,NN LD (BC),A		
03 04 05	INC BC INC B DEC B		
0620 07 08	LD B,N		
09 0A 0B 0C	RLCA EX AF,AF' ADD HL,BC LD A,(BC) DEC BC INC C DEC C		
0D 0E20 0F	DEC C LD C,N RRCA DJNZ DIS		
102E 118405 12	DJNZ DIS LD DE,NN LD (DE) A		
13 14 15	LD DE,NN LD (DE),A INC DE INC D DEC D		
1620 17 182E	RLA		
19 1A 1B	ADD HL,DE LD A,(DE) DEC DE INC E		
1C 1D 1E20	LD E,N		
1F 202E 218405 228405 23	RRA JR NZ,DIS LD HL,NN		
23 24 25	JR NZ,DIS LD HL,NN LD (NN),HL INC HL INC H DEC H		
2620 27 282E	LD H,N DAA JR Z DIS		
29 2A8405 2B	LD H,N DAA JR Z,DIS ADD HL,HL LD HL,(NN) DEC HL INC L DEC L LD L N		
2C 2D 2E 20			
2F 302E 318405 328405	CPL JR NC,DIS LD SP,NN		
328405 1 33 34 35 3620	CPL JR NC,DIS LD SP,NN LD (NN),A INC SP INC (HL) DEC (HL) LD (HL),N SCF IR C DIS		
3620 37 382E	LD (HL),N SCF		
39 3A8405 3B	JR C,DIS ADD HL,SP LD A,(NN) DEC SP		
3C 3D 3E20	LD A,(NN) DEC SP INC A DEC A LD A,N		
3F 40 41	LD A,N CCF LD B,B LD B,C		
42 43 44 45	LD B,E		
45 46 47 48	LD B,(HL) LD B,(HL) LD B,A		
49 4A 4B	LD C,B LD C,C LD C,D LD C,E		
4C 4D 4E	LD C,H LD C,L LD C,(HL)		
4F 50 51	LD C,A LD D,B LD D,C LD D,D		
52 53 54	LD D,E LD D,H		
55 56 57	LD D,L LD D,(HL) LD D,A		

OPR	OCESS	80
OBJ CODE	SOURCE STATEMENT	T.
58 59	LD E,B	ı
5A	LD E,D	1
5B 5C	LD E,H	
5D 5E	LD E,L	
5F 60	LD E.A LD H,B	
61 62	LD E,A LD H,B LD H,C LD H,D	
63 64	LD H,E LD H,H	
65	LD H,L	H
66 67	LD H,(HL) LD H,A	
68 69	LD L,B LD L,C	H
6A 6B	LD L,D LD L,E	ŀ
6C 6D	LD L,H LD L,L	
6E 6F		
70 71	I D (ULL) B	
72 73	LD (HL),D LD (HL),E	
74 75	LD (HL),C LD (HL),D LD (HL),E LD (HL),H LD (HL),L	
76	HALI	
77 78	LD (HL),A LD A,B	
79 7A	LD A,C LD A,D LD A,E	
7B 7C	LD A.H	
7D 7E	LD A (HL)	H
7F 80	ADD A.B	
81 82	ADD A,C ADD A,D	H
83 84	ADD A,E	Н
85	ADD A L	П
86 87	ADD A.A	Н
88 89	ADC A,B ADC A,C	
8A 8B	ADC A.D ADC A.E	
8C 8D	ADC A,H ADC A,L	Н
8E 8F	ADC A,(HL) ADC A.A	H
90 91	SUB B SUB C	Н
92 93	SUB D SUB E	Н
94 95	SUB H SUB L	Н
96 97	SUB (HL) SUB A	П
98	SBC A.B	П
99 9A	SBC A,C SBC A,D	П
9B 9C	SBC A,E SBC A,H	
9D 9E	SBC A,L SBC A,(HL)	1
9F A0	SBC A,A AND B	П
A1 A2	AND C AND D	Н
A3 A4	AND E AND H	Н
A5 A6	AND L AND (HL)	Н
A7 A8	AND A XOR B	П
A9 AA	XOR C XOR D	П
AB AC	XOR E XOR H	П
AD	XOR L	П
AE AF	XOR A	Н
B0 B1	OR B OR C	ij
B2 B3	OR D OR E	ı
B4 B5	OR H OR L	H
B6 B7	OR (HL) OR A	I
		•

OR II	NSTRU	CTIC
OBJ CODE	SOURCE STATEMENT	COD
B8 B9	CP B CP C	CB55 CB56
BA BB	CP D CP E	CB57 CB58
BC BD	CP H CP L	CB59 CB5A
BE BF	CP (HL)	CB5B CB5C
CO C1	CP A RET NZ POP BC	CB5D
C28405 e	JP NZ NN	CB5E CB5F
C38405 C48405	JP NN CALL NZ,NN	CB60 CB61
C5 C620	PUSH BC ADD A,N	CB62 CB63
C7 C8	RST O	CB64 CB65
C9 CA8405	RET JP Z,NN	CB66
CB00 *	RLC B	CB67 CB68
CB01 CB02	RLC C RLC D	CB69 CB6A
CB03 CB04	RLC E RLC H	CB6B CB6C
CB05 CB06	RLC L RLC (HL)	CB6C
CB07	RLC A	CB6E CB6F
CB08	RRC B RRC C	CB70 CB71
CB0A CB0B	RRC D RRC E	CB72 CB73
CB0C	RRC H RRC L	CB74 CB75
CB0E	RRC (HL)	CB76
CB0F CB10	RL B	CB77 CB78
CB11 CB12	RL C RL D	CB79
CB13 CB14	RL E RL H	CB78 CB70
CB15 CB16	RL L RL (HL)	CB7E
CB17 CB18	RL A RR B	CB7F
CB19	RR C	CB80 CB81
CB1A CB1B	RR D RR E	CB82 CB83 CB84
CB1C CB1D	RR H RR L	CB84 CB85
CB1E CB1F	RR L RR (HL) RR A	CB86 CB87
CB20 CB21	SLA B SLA C	CB88 CB89
CB22	SLA D	CB8A
CB23 CB24	SLA E SLA H	CB8B
CB25 CB26	SLA L SLA (HL)	CB8E
CB27 CB28	SLA A SRA B	CB8F CB90
CB29 CB2A	SRA C	CB91 CB92
CB2B	SRA D SRA E	CB93
CB2C CB2D	SRA H SRA L	CB94 CB95
CB2E CB2F	SRA (HL) SRA A	CB96 CB97
CB38 CB39	SRL B SRL C	CR98 CB99
CB3A	SRL D	CB9A
CB3C	SRL E SRL H	CB9B CB9C
CB3D CB3E	SRL L SRL (HL)	CB9C CB9E
CB3F CB40	SRL A BIT 0,B	CB9F CBAC
CB41 CB42	BIT O,C BIT O,D	CBA1
CB42 CB43 CB44	BIT O,E BIT O,H	CBA3
CB45	BIT O.L	CBAS
CB46 CB47	BIT 0,(HL) BIT 0,A BIT 1,B	CBAC CBA
CB48 CB49	BII I,C	CBAS CBAS
CB4A CB4B	BIT 1,D BIT 1 E	CBA
CB4C CB4D	BIT 1,H BIT 1,L	CBAC CBAC
CB4E	BIT 1 (HL)	CBA
CB4F CB50	BIT 1,A BIT 2,B	CBA1
CB51 CB52	BIT 2,C BIT 2 D	CBB1 CBB2
CB53 CB54	BIT 2,E BIT 2,H	CBB3 CBB4
0004	Q	<u> 7555,</u>

OBJ	SOURCE
CODE CB55	STATEMENT
CB56	
CB57 CB58	BIT 2.(HL) BIT 2.A BIT 3.B BIT 3.C BIT 3.D BIT 3.E BIT 3.H
CB59	BIT 3,C BIT 3,D
CB5A CB5B	BIT 3,E
CB5C CB5D	BIT 3,E BIT 3,H
CB5E CB5F	BIT 3,L BIT 3,(HL)
CB5F CB60	BIT 3,A BIT 4,B
CB61 CB62	BIT 4,C BIT 4,D
CB63	BIT 4,E BIT 4,H
CB64 CB65	BIT 3: L BIT 3: (HL) BIT 3: AB BIT 4: B BIT 4: B BIT 4: L BIT 4: L BIT 4: L BIT 4: L BIT 5: B BIT 5: D BIT 5: D BIT 5: L BIT 5: L
CB66 CB67	BIT 4,L BIT 4,(HL)
CB68	BIT 5,B
CB69 CB6A	BIT 5,C BIT 5 D
CB6B	BIT 5,E
CB6C CB6D CB6E	BIT 5,L
	BIT 5,(HL) BIT 5 A
CB70	BIT 6,B
CB70 CB71 CB72 CB73	0110,0
CB73 CB74	BIT 6,E BIT 6 H
CB74 CB75	BIT 6,D BIT 6,E BIT 6,H BIT 6,L BIT 6,C BIT 7,B BIT 7,B BIT 7,D BIT 7,D BIT 7,H BIT 7,H BIT 7,H BIT 7,H BIT 7,H
CB76 CB77	BIT 6,(HL)
CB78	BIT 7,8
CB77 CB78 CB79 CB7A CB7B CB7C	BIT 7,D
CB7B CB7C	BIT 7,E BIT 7.H
CB/D	BIT 7,L
CB7E CB7F	BIT 6, (HL) BIT 6, A BIT 7, B BIT 7, C BIT 7, C BIT 7, C BIT 7, E BIT 7, H BIT 7, H BIT 7, (HL) BIT 7, A
CB80 CB81	
CB82	00000
CB82 CB83 CB84	RES O.E RES O.H RES O.L RES O.(HL) RES O.A
CB85 CB86	RES 0,H RES 0,L RES 0,(HL) RES 0,A RES 1,B RES 1,C RES 1,C RES 1,E RES 1,H
CB87 CB88	RES O.A RES 1,B
CB89	RES 1,B
CB8A CB8B	RES 1,D
CB8C	RES 1,E RES 1,H
CB8D CB8F	RES 1,L RES 1,(HL)
CB8F CB90	RES 1,A RES 2,B RES 2,C
CB91 CB92	RES 2,C
CB93	RES 2,D RES 2,E
CB94 CB95	RES 2,H RES 2,L
CB96	RES 2,(HL)
CB97 CB98	RES 2,A RES 3,B
CB99 CB9A	RES 1. A HES 7. B RES 2.C RES 2.C RES 2.E HES 2.H RES 2.L HES 2.A HES 3.C HES 3.D HES 3.D HES 3.H HES 3.H HES 3.L HES 3.L HES 3.L HES 3.L
СВ9В	RES 3,E
CB9C CB9D	RES 3,H RES 3 L
CB9E CB9F	RES 3,(HL)
CBA0	RES 3,D RES 3,E RES 3,H RES 3,L RES 3,(HL) RES 3,A RES 4,B
CBA1 CBA2	RES 4,C RES 4 D
CBA3 CBA4	RES 4,E RES 4,H
CBA5 *	RES 4,L
CBA6 CBA7	RES 4,L RES 4,(HL) RES 4,A
CBA8 CBA9	RES 5,B RES 5,C
CDAA	RESSO
CBAB	RES 5.D RES 5.E RES 5.H
CBAD	RES 5,L RES 5 (HL)
CBAF	RES 5,(HL) RES 5,A
CBBO	RES 6,B RES 6,C
CBB2	RES 5,E RES 5,H RES 5,(HL) RES 5,(HL) RES 5,A RES 6,B RES 6,C RES 6,D RES 6,E
CBB3 CBB4	RES 6,E RES 6,H

OBJ CODE	SOURCE STATEMENT
CBB5 CBB6	RES 6,L RES 6,(HL) RES 6,A
CBB7 CBB8	RES 6,A RES 7,B
CBB9 CBBA	RES 7.C RES 7.D
CBBB CBBC	RES 7,E RES 7,H
CBBD	RES 7,L RES 7,(HL)
CBBF CBC0	RES 7, A SET 0,B
CBBF CBC0 CBC1 CBC2 CBC3 CBC4	SET O,C SET O,D
CBC3 CBC4	SET O,E SET O,H
CBC5 CBC6 CBC7 CBC8 CBC9 CBCA CBCB CBCC	SET O,(HL)
CBC7	SET 1,B
CBCA	SET 1,D
CBCC	SET 1,H
CRCF	SET 1,(HL)
CBCF CBD0	SET 2,B
CBD1 CBD2 CBD3	SET 2,D
CRD4	SET 2,H
CBD6	SET 2,(HL)
CBD8	SET 3,B
CBDA	SET 3.D SET 3.F
CBDC	SET 3.H
CBD5 CBD6 CBD7 CBD8 CBD9 CBDA CBDB CBDC CBDC CBDC CBDE CBDF	SET 3,(HL) SET 3.A
CBE0	SET 4,B SET 4.C
CBE2 CBE3 CBE4 CBE5 CBE6 CBE6 CBE7 CBE8 CBE9 CBE9	HESS, AB HESS,
CBE4 CBE5	SET 4,H SET 4,L
CBE6 CBE7	SET 4,(HL) SET 4,A
CBE8 CBE9	SET 5,B SET 5,C
CBEA	SET 5,D SET 5,E
CBED CBED	SET 5,H SET 5,L
CBEE CBEF CBF0 CBF1 CBF2 CBF3 CBF4	SET 5,(HL) SET 5,A
CBF0 CBF1	SET 6,B SET 6,C
CBF2 CBF3	SET 6,D SET 6,E
	SET 6.L
CBF6 CBF7 CBF8	SET 6,A
GBF9 CBFA	SET 7,C
CBFB	SET 7,E
CBFC CBFE CBFF	SET 7,L SET 7,(HL)
CBFF	SET 6,E SET 6,H SET 6,L SET 6,L SET 6,HL SET 7,B SET 7,C SET 7,C SET 7,D SET 7,C SET 7,L SET 7,L SET 7,L SET 7,L SET 7,L SET 7,L SET 7,L SET 7,L SET 7,L
CC8405 CD8405 CE 20 CF	CALL Z,NN CALL NN
	RST 8
D1 D28405	POP DE
D1 D28405 D320 D48405	OUT (N) A
D5 D620	PUSH DE SUB N
D7 D8	RST 10H RET C
D9 DA8405	EXX JP C.NN
DB20 DC8405	IN A,(N) CALL C.NN
DD09	ADD IX BC
DD218405 DD228405	ADC AN RST B RST B NC POP DE JP NC, NN OUT (IN), A CALL NC, NN PUSH DE SUB N RST 10H RET C EXX POP NN IN A, (IN) CALL C, NN ADD IX, DE LD IX, NN LD

CODE	SOURCE STATEMENT
DD23 DD29 DD2A8405	INCIX
DD29 DD2A8405 DD28 DD3405 DD3505 DD360520 DD39	ADD IX,IX  LD IX, (IN)  DEC IX  DEC (IX+d)  DEC (IX+d)  DEC (IX+d)  DEC (IX+d)  DEC (IX+d)  LD IX-Y-d)  SEC IX-
DD3505 DD360520	DEC (IX+d) LD (IX+d),N
DD4605	ADD IX,SP LD B,(IX+d)
DD5605 DD5E05	LD D,(IX+d)
DD6605 DD6E05	LD H,(IX+d)
DD7005 DD7105	LD (IX+d),B LD (IX+d),C
DD6E05 DD7005 DD7105 DD7205 DD7305 DD7305 DD7405 DD7506 DD7705 DD7705 DD7E05 DD8605	LD (IX+d),E LD (IX+d),H
DD7505 DD7705	LD (IX+d),L
DD8605 DD8E05	ADD A,(IX+d)
DD9605 DD9E05	SBC A,(IX+d)
DDA605 DDAE05	AND (IX+d) XOR (IX+d)
DDB605 DDBE05	CP (IX+d)
DDE1 DDE3 DDE5	EX (SP), IX PUSH IX
DDE9 DDF9	JP (IX) LD SP,IX
DDE3 DDE5 DDE9 DDF9 DDC80506 DDC80506 DDC80516 DDC80516	REC (IX+d)
DDCB051E DDCB0526	RR (IX+d) SLA (IX+d)
DDCB052E DDCB053E	SRA (IX+d) SRL (IX+d)
DDC80526 DDC8052E DDC8053E DDC80546 DDC80546 DDC80556 DDC80556	BIT 0.(IX+d) BIT 1.(IX+d)
DDC8055E DDC80566	BIT 2,(IX+d) BIT 3,(IX+d) BIT 4 (IX+d)
DDC8055E DDC8056E DDC8056E DDC80576 DDC8057E DDC8058E DDC8058E DDC8059E DDC8059E	B(T 5,(IX+d) B(T 6,(IX+d)
DDC8057E DDC80586	BIT 7,(IX+d) RES 0,(IX+d)
DDCB058E DDCB0596	RES 1.(IX+d) RES 2.(IX+d)
DDCB05A6 DDCB05AE DDCB05B6	RES 4,(IX+d) RES 5,(IX+d)
DDCB05B6 DDCB05BE	RES 6,(1X+d) RES 7,(1X+d)
DDCB05B6 DDCB05BE DDCB05C6 DDCB05C6 DDCB05D6 DDCB05D6 DDCB05E6 DDCB05E6	SET 0,(IX+d) SET 1,(IX+d)
DDCB05DE DDCB05E6	SET 3,(1X+d) SET 4,(1X+d)
DDCB05F6 DDCB05F6 DDCB05FE	SET 5 (IX+d) SET 6 (IX+d)
DE 20	SET 7,(IX+d) SBC A,N BST 18H
E0 E1 E28405 E3	RET PO POP HL
E 28405 E 3	JP PO,NN EX (SP),HL
E 48405 E 5 E 620	PUSH HL
E 7 E 8	RST 20H RET PE
E9 EA8405	JP (HL) JP PE,NN
EC8405 ED40	CALL PE,NN
ED41 ED42	RST 18H RET PO POP HL JP PO, NN PUSH HL AND N RST 20H RET PE JP HL JP PE, NN EX DE, HL CALL PE, NN IN B, (C) OUT (C) B SBC HL, BC LD (NN), BC LD (NN), BC
ED438405 ED44	NEG
ED45 ED46 ED47	IM O
ED48	IN C,(C) OUT (C),C
ED4A ED4B8405 ED4D	ADC HL,BC LD BC,(NN)
ED4D ED50 ED51	RETN IM 0 LD I,A IN C,(C) OUT (C),C ADC HL,BC LD BC,(NN) RETI IN D,(C) OUT (C),D
-54.	301 101,0

OBJ CODE	SOURCE STATEMENT
	SBC HL,DE LD (NN),DE
ED52 ED538405 ED56	LD (NN),DE
ED56 ED57 ED58	IM 1 LD A I
ED58	LD A,I IN E,(C) OUT (C),E ADC HL,DE
ED59 ED5A	OUT (C),E ADC HL.DE
ED5B8405	LD DE (NN)
ED5A ED5B8405 ED5E ED60 ED61 ED62 ED67 ED68	LD DE,(NN) IM 2 IN H,(C) OUT (C),H SBC HL,HL
ED61	OUT (C),H
ED62	SBC HL,HL RRD
ED68	IN L.(C)
ED69	OUT (C),L
ED6F	RLD
ED72 ED738405	SBC HL,SP
ED78	RRD IN L.(C) OUT (C),L ADC HL,HL RLD SBC HL,SP LD (NN),SP IN A.(C) OUT (C),A ADC HL,SP LD SP,(NN) LDI CPI
ED79	OUT (C),A
ED7B8405	LD SP.(NN)
EDA0	LDI CPI
EDA2	INI
EDA3	OUTI
EDA9	INI OUTI LOD CPD
ED69 ED6A ED6F ED72 ED738405 ED79 ED7A ED7B405 EDA1 EDA2 EDA3 EDA8 EDA9 EDAA EDAB EDAB EDAB EDAB EDB0 EDB1 EDB1	IND OUTD
EDB0	LDIR CPIR
EDB1	CPIR
EDB2 EDB3	INIR OTIR
EDB8 EDB9	LDDR
EDB9 EDBA	CPDR
EDBA EDBB EE20 EF	INDR OTDR XOR N RST 28H RET P POP AF
EE20	XOR N RST 28H
F0 F1	RETP
F1	POP AF
F28405 F3	JP P.NN DI
F48405 F5	CALL P,NN
F620	CALL P,NN PUSH AF OR N
F7 F8	RST 30H
F9	LD SP.HL
FA8405	IP M NN
FB FC8405	
FD09	ADD IY,BC
FD19 FD218405	ADD IY,DE LD IY,NN LD (NN),IY INC IY
FD218405 FD228405 FD23 FD29 FD2A8405	LD (NN),IY
FD23	ADD IV IV
FD2A8405	LD IY,(NN)
FD2B	DEC IY
FD3505	DEC (IY+d)
FD2B FD3405 FD3505 FD360520 FD39 FD4605 FD4E05 FD5605	LD (IY+d),N
FD4605	LD B,(IY+d)
FD4E05 FD5605	LD C (IV+d)
FD5E05	LD E,(IY+d)
FD6605	LD H (IY+d)
FD7005	LD ((Y+d) LD ((Y+d).B
FD6605 FD6E05 FD7005 FD7105 FD7205 FD7305 FD7405 FD7505 FD7705 FD7E05 FD7E05	LD H, ((Y+d) LD L, ((Y+d), B LD ((Y+d), C LD ((Y+d), C LD ((Y+d), E LD ((Y+d), H LD ((Y+d), L
FD7205 FD7305	LD (IY+d),D
FD7405	LD (IV+d),H
FD7705	LD (IY+d),L LD (IY+d) A
FD7E05	LD A (IY+d)
FD8E05	ADD A,(IY+d)
FD9605	SUB (IY+d)
FD9E05 FDA605	SBC A,(IY+d) AND (IY+d)
FDAE05	XOR (IY+d)
FDB605 FDBE05	OR (IV+d)
FDE1 FDE3	POPIY
FDE3 FDE5	EX (SP),IY
FDE9	JP (IY)
FDF9	(INC IY ADD IY./ (NI) DEC IY INC (IY-d) DEC IY INC (IY-d) LD (IY-d) ADD IY-d) ADD IX-d ADD
FDCB0506 FDCB050E FDCB0516	RRC (IY+d)
FDCB0516	RL (IY+d)

OBJ	SOURCE
CODE	STATEMENT
FDCB051E	BB ((Y+d)
FDCB0526	SLA (IY+d)
FDCB052E	SRA (IY+d)
FDCB053E	SRL (IY+d)
FDCB0546	BIT 0.(IY+d)
FDCB054E	BIT 1,(IY+d)
FDCB0556	BIT 2 (IY+d)
FDCB055E	BIT 3,(IY+d)
FDCB0566	BIT 4,(IY+d)
FDCB056E	BIT 5,(IY+d)
FDCB0576	BIT 6,(IY+d)
FDCB057E	BIT 7,(IY+d)
FDCB0586	RES 0,(1Y+d)
FDCB058E	RES 1,(IV+d)
FDCB0596	RES 2,(IY+d)
FDCB059E	RES 3,(IY+d)
FDCB05A6	RES 4.(IY+d)
FDCB05AE	RES 5,(IY+d)
FDCB05B6	RES 6,(IY+d)
FDCB05BE	RES 7,(IY+d)
FDCB05C6	SET O, (IY+d)
FDCB05CE	SET 1,(IY+d)
FDCB05D6 FDCB05DE	SET 2.(IY+d)
FDCB05E6	SET 3,(IY+d) SET 4,(IY+d)
FDCB05E6	SET 5.(IY+d)
FDCB05F6	SET 6.(IY+d)
FDCB05F6	SET 7.(IY+d)
FE20	CP N
FF	RST 38H

Z80-CPU
INSTRUCTIONS
SORTED BY
MNEMONIC

OBJ	SOURCE
CODE	STATEMENT
8E	ADC A,(HL)
DD8E05	ADC A,(IX+d)
FD8E05	ADC A,(IY+d)
8F	ADC A,A
88	ADC A,B
89	ADC A,C
8A	ADC A,D
8B	ADC A.E
8C	ADC A.H
. 8D	ADC A,L ADC A,N
CE20 ED4A	ADC HLBC
ED5A	ADC HL,BC
ED6A	ADC HL,HL
ED7A	ADC HL,SP
86	ADD A (HL)
DD8605	ADD A,(IX+d)
FD8605	ADD A,(IY 'd)
87	ADD A,A
80	ADD A,B
81	ADD A,C
82	ADD A,D
83	ADD A,E
84	ADD A,H
85	ADD A.L
C620	ADD A.N
09 19	ADD HL,BC ADD HL,DE
29	ADD HL,HL
39	ADD HL,SP
DD09	ADD 1X.BC
DD19	ADD IX.DE
DD29	ADD IX.IX
DD39	ADD IX,SP
FD09	ADD IY.BC
FD19	ADD IY,DE
FD29	ADD IY,IY
FD39	ADD IY,SP
A6	AND (HL)
DDA605	AND (IX+d)
FDA605	AND (IY+d)
A7	AND A
A0	AND B
A1 A2	AND C AND D
A3	AND E
A4	AND H
A5	AND L
E620	AND N
CB46	BIT O.(HL)
DDCB0546	BIT 0,(IX+d)
FDC80546	BIT 0 (IV+d)

ОВЈ	SOURCE STATEMENT	
CODE		
CB47 CB40	BIT O,A BIT O,B	
CB41 CB42	BIT O,C BIT O,D	
CB43		
CB44 CB45	BIT O,H BIT O,L	
CB4E	BIT 0,L BIT 1,(HL) BIT 1,(IX+d) BIT 1,(IY+d)	
DDC8054E FDCB054E	BIT 1,(IX+d) BIT 1,(IX+d)	
CB4F BC48	BILLA T	
CB49	BIT 1.C	
CB4A CB4B	BIT 1,D BIT 1,E	
CB4C CB4D	BIT 1,H	
CB56	BIT 1,H BIT 1,L BIT 2,(HL) BIT 2,(IX+d) BIT 2,(IY+d) BIT 2,A BIT 2,B	
DDCB0556 FDCB0556 CB57	BIT 2,(HL) BIT 2,(IX+d) BIT 2,(IY+d)	
CB57 CB50	BIT 2,A BIT 2,B	
CB50 CB51 CB52		
CB52 CB53		
	BIT 2,H	
CB55 CB5E	BIT 2,L BIT 3,(HL)	
DDCB055E FDCB055E	BIT 2,H BIT 2,L BIT 3,(HL) BIT 3,(IX+d) BIT 3,(IX+d)	
CB5F	BIT 3,4 BIT 3,B	
CB58 CB59	BIT 3,A BIT 3,B BIT 3,C BIT 3,D BIT 3,E BIT 3,H BIT 3,L	
CB5A CB5B	BIT 3,D	
CB5C	BIT 3,E BIT 3,H	
CB5D CB66		
DDCB0566	BIT 4,(IX+d) BIT 4,(IY+d)	
FDCB0566 CB67	DITAA	
CB60 CB61	BIT 4,B BIT 4,C	
CB62	RIT 4 D	
CB64	RITAH	
CB65 CB6E		
DDCB056E	BIT 5,(HL) BIT 5,(IX+d) BIT 5,(IY+d) BIT 5,A	
FDCB056E CB6F	BIT 5,(IY+d) BIT 5,A	
CB68 CB69	BIT 5,B BIT 5,C	
CB6A	BIT 5,D	
CB6B CB6C	BIT 5,F BIT 5,H	
CB6D CB76	BIT 5,L	
DDCB0576 FDCB0576	BIT 6,(HL) BIT 6,(IX+d) BIT 6,(IY+d)	
CB77	BIT 6.A	
CB70 CB71	BIT 6,B BIT 6,C	
CB72	BIT 6 D	
CB73 CB74		
CB75 CB7E	BIT 6,L BIT 7,(HL)	
DDCB057F	BIT 6,H BIT 6,L BIT 7,(HL) BIT 7,(IX+d) BIT 7,(IY+d)	
FDCB057E CB7F	BIT 7,(IY+d) BIT 7,A	
CB78	BIT 7,A BIT 7,B BIT 7,C BIT 7,D BIT 7,E	
CB79 CB7A CB7B	BIT 7.D	
■ CB7C	BIT 7.A BIT 7.B BIT 7.C BIT 7.D BIT 7.E BIT 7.H BIT 7.H CALL C.NN	
CB7D DC8405	BIT 7,H BIT 7,L CALL C,NN	١
FC8405	CALL M,NN	١
D48405 CD8405	CALL NC,NN CALL NN	١
C48405 F48405	CALL NZ,NN	ı
FC8405	CALL PE NN	l
E48405 CC8405	CALL P,NN CALL PE,NN CALL PO,NN CALL Z,NN	ı
3F BE	CCF CR (HL)	
DOBE05	CP (IX+d)	
FDBE05 BF	CP A	ı
88 89	CP B	١
BA BB	CP D CP E	١
BB BC	CP E	
•		1

BD	1	OBJ CODE	SOURCE STATEMENT		OBJ CODE	SOU STATE
EDA9 CPDR D07505 LD (1)   EDB9 CPDR D07505 LD (1)   EDB1 CPIR F07005 LD (1)   EDB2 CE (14'-d) F07505 LD (1)   EDB2 CE CE CD 228405 LD (1)   EDB28405 LD	1	BD	CP L CP N	۱	DD7305	
EOB1	н	EDA9	CPD	ı	DD7505	I D IIX
EOB1		EDA1	CPI	П	FD7705	LD (IX
27 DAA  35 DEC (IHL)  36 DEC (IHL)  57 DEC (IY-I)  37 DEC (IY-I)  58 DEC (IY-I)  59 DEC (IY-I)  50 DEC A  50 DEC B  51 DEC DE  51 DEC DE  52 DEC H  52 DEC H  53 DEC H  54 DEC B  55 DEC II  56 DEC B  57 DEC B  58 DEC H  59 DEC B  50 DEC	ı	EDB1 2F	CPL	l	FD7005	LD (IY
3D DEC A FD360520 LD ( 06 DEC B 228405 LD ( 07 DEC C ED538405 LD ( 18 DEC DE D28405 LD ( 18 DEC DE D28405 LD ( 18 DEC DE D28405 LD ( 19 DEC E ED538405 LD ( 19 DEC E ED548405 LD ( 19 DEC E ED548405 LD ( 19 DEC E ED548405 LD ( 19 DES58405 LD ( 19 DE5685 LD ( 19 DES58405 LD ( 19	ı	27	DAA		FD7205	LD (IY
3D DEC A FD360520 LD ( 06 DEC B 228405 LD ( 07 DEC C ED538405 LD ( 18 DEC DE D28405 LD ( 18 DEC DE D28405 LD ( 18 DEC DE D28405 LD ( 19 DEC E ED538405 LD ( 19 DEC E ED548405 LD ( 19 DEC E ED548405 LD ( 19 DEC E ED548405 LD ( 19 DES58405 LD ( 19 DE5685 LD ( 19 DES58405 LD ( 19	ı	DD3505	DEC (IX+d)	ı	FD7405	LD (IY
OF   DEC B   S28405   LO (  OF   OF   OF   OF   OF   OF   OF	1	3D	DEC A		FD7505 FD360520	LD (IV
DO	Ш		DEC B		328405	LD (NN
18		0D	DECC	Н	ED538405	LD (NN
Total   Tota		18	DEC DE		DD228405	LD (NN
Total   Tota		25	DEC E DEC H		FD228405 ED738405	LD (NN
Total   Tota	П		DEC HL	П	0A	LD A.II
Total   Tota		FD2B	DECIY	П	7E	LD A,(I
Total   Tota		3B	DCC 31	1	FD7E05	LD A,(
FB EI S EX (SP),HL 79 DAS EX (SP),1X 7A DAS EX (			DI DJNZ DIS		3A8405 7F	
Formal			E1 (SP) HI		78	LD A,B
Formal		DDE3	EX (SP),IX		7A	LD A,D
Formal		08	LX AL AL		7C	LD A,E
76		EB D9	EXX		ED57	LD A,I
ED56	П	76	HALT		3E20	LD A,N
ED78 IN A.(C) 47 LD B DB20 IN A.(N) 40 LD B ED40 IN B.(C) 41 LD B ED50 IN D.(C) 42 LD B ED50 IN D.(C) 42 LD B ED50 IN D.(C) 42 LD B ED50 IN D.(C) 43 LD B ED50 IN D.(C) 43 LD B ED50 IN D.(C) 44 LD B ED50 IN D.(C) 45 LD B ED50 IN H.(C) 45 LD B ED60 IN H.(C) 45 LD B ED60 IN H.(C) 45 LD B ED60 IN H.(C) 45 LD B ED62 IN C (IV+d) 4E LD C ED62 IN C ED62 ID C ED63 IN C ED64 IN C ED64 IN C ED64 IN C ED65 IN C ED65 ID C ED65 IN C ED65 ID C ED66 IN C ED65 ID C ED66 IN C ED66 ID C ED67 IN C ED66 ID C ED67 IN C ED66 ID C ED67 IN C ED66 ID C ED68 IN ED66 ID C ED68 IN ED66 ID C ED69 IP (IV) 54 LD C ED68 IN ED66 ID C ED68 IN C ED66 ID C ED68 IN ED66 ID C ED68 IN C ED66 ID C ED68 ID C ED68 IN C ED66 ID C ED68		ED56	IM 1		DD4605	ID B (1
ED48		ED78	IN A,(C)		FD4605 47	LD B,(I
ED48 IN C.(C) 42 LD 8 ED56 IN D.(C) 43 LD 8 ED56 IN E.(C) 44 LD 8 ED56 IN H.(C) 45 LD 8 ED56 IN L.(C) 650 B ED56 IN L.(C) 6620 B ED58 INC (IX+d) 018405 LD 8 B ED58 INC (IX+d) 018405 LD 8 B ED58 INC (IX+d) 67 B ED58 INC B ED58 INC B B B B B B B B B B B B B B B B B B B		DB20 ED40	IN A,(N)		40	LD B,B
ED68 IN L.(C) 0620 LD 8 34 INC (HLI D18405 LD 8 D03405 INC (IY+d) 4E LD 18 CO INC A DD4605 LD C OA INC B FD4605 LD C OA INC B FD4605 LD C OA INC C 48 LD C 14 INC D 49 LD C 14 INC D 49 LD C 16 INC C 48 LD C 17 INC C 48 LD C 18 INC DE 4A LD C 19 INC C 48 LD C 19 INC C 48 LD C 10 INC C 48 LD C 10 INC C 48 LD C 11 INC DE 4A LD C 11 INC DE 4A LD C 12 INC C 4B LD C 12 INC C 4B LD C 13 INC H 4C LD C 14 INC D 49 LD C 15 INC C 18 LD C 16 LD D5605 LD C 17 INC C 18 LD		ED48	IN C.(C)		42	LD B,D
ED68 IN L.(C) 0620 LD 8 34 INC (HLI D18405 LD 8 D03405 INC (IY+d) 4E LD 18 CO INC A DD4605 LD C OA INC B FD4605 LD C OA INC B FD4605 LD C OA INC C 48 LD C 14 INC D 49 LD C 14 INC D 49 LD C 16 INC C 48 LD C 17 INC C 48 LD C 18 INC DE 4A LD C 19 INC C 48 LD C 19 INC C 48 LD C 10 INC C 48 LD C 10 INC C 48 LD C 11 INC DE 4A LD C 11 INC DE 4A LD C 12 INC C 4B LD C 12 INC C 4B LD C 13 INC H 4C LD C 14 INC D 49 LD C 15 INC C 18 LD C 16 LD D5605 LD C 17 INC C 18 LD		ED58	IN E.(C)		44	LD B,H
D03405   INC (IX+d)   D18405   D8		ED68	IN H,(C)		9620	LD B,L LD B,N
10			INC (HL)		ED488405	LD BC,
10		FD3405	INC (IY+d)		4E	LD C,(i
10		04	INC B		FD4E05	LD C,(I
10		OC.	INC C	li		LD C,A
10			INC D INC DE		49	LD C,C
10		1C	INC E		4B	LD C,E
10		23	INC HL		4D	LD C,L
33 INC SP FD5605 LD D EDBA INDD 57 EDBA INDD 57 EDBA INDR 50 EDBA ININ 51 EDB2 INIR 52 EDB2 INIR 54 EDB2 INIR 55 EDB2 INIR		FD23	INC IX INC IY			LD C,N LD D,(I
FDE9 JP HY) 35 LUD DAB405 JP C.NN 1620 LDD DAB405 JP C.NN 1620 LDD DAB405 JP NC.NN 1620 LDD DAB405 JP NC.NN 118405 LDD C28405 JP NC.NN 118405 LDD C28405 JP NX.NN DDSE05 LDE C28405 JP NX.NN DDSE05 LDE C28405 JP P.NN NP.NN DDSE05 LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP C.NN SP LDE C28405 JP			INC L INC SP		DD5605	LD D (
FDE9 JP HY) 35 LUD DAB405 JP C.NN 1620 LDD DAB405 JP C.NN 1620 LDD DAB405 JP NC.NN 1620 LDD DAB405 JP NC.NN 118405 LDD C28405 JP NC.NN 118405 LDD C28405 JP NX.NN DDSE05 LDE C28405 JP NX.NN DDSE05 LDE C28405 JP P.NN NP.NN DDSE05 LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP C.NN SP LDE C28405 JP		EDAA	IND		57	LD D.A
FDE9 JP HY) 35 LUD DAB405 JP C.NN 1620 LDD DAB405 JP C.NN 1620 LDD DAB405 JP NC.NN 1620 LDD DAB405 JP NC.NN 118405 LDD C28405 JP NC.NN 118405 LDD C28405 JP NX.NN DDSE05 LDE C28405 JP NX.NN DDSE05 LDE C28405 JP P.NN NP.NN DDSE05 LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP C.NN SP LDE C28405 JP		EDA2	INI		51	LD D,C
FDE9 JP HY) 35 LUD DAB405 JP C.NN 1620 LDD DAB405 JP C.NN 1620 LDD DAB405 JP NC.NN 1620 LDD DAB405 JP NC.NN 118405 LDD C28405 JP NC.NN 118405 LDD C28405 JP NX.NN DDSE05 LDE C28405 JP NX.NN DDSE05 LDE C28405 JP P.NN NP.NN DDSE05 LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP P.NN SP LDE C28405 JP C.NN SP LDE C28405 JP		E9	JP (HL)		53	LD D,D
DAB405	ı	FDF9	JP (IX) JP (IY)	П	54	LDD,H
E28405 JP PO.NN 58 LD E CA8405 JP Z.NN 59 LD E 382E JR C.DIS 5A LD E 1822 JR DIS 58 LD E 302E JR NC.DIS 5D LD E 202E JR NZ.DIS 5D LD E 202E JR NZ.DIS 5D LD E 202E JR Z.DIS 5D LD E 12 LD IBC.J 66 LD H 12 LD IBC.J 66 LD H 77 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B LD H 71 LD IHLI, B LD H 72 LD IHLI, B LD H 73 LD IHLI, B LD H 74 LD IHLI, B LD H 75 LD IHLI, B LD H 75 LD IHLI, B LD H 76 LD IHLI, B LD H 77 LD IHLI, B LD H 77 LD IHLI, B LD H 78 L	ı	DA8405	JP C,NN			LD D.N
E28405 JP PO.NN 58 LD E CA8405 JP Z.NN 59 LD E 382E JR C.DIS 5A LD E 1822 JR DIS 58 LD E 302E JR NC.DIS 5D LD E 202E JR NZ.DIS 5D LD E 202E JR NZ.DIS 5D LD E 202E JR Z.DIS 5D LD E 12 LD IBC.J 66 LD H 12 LD IBC.J 66 LD H 77 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B LD H 71 LD IHLI, B LD H 72 LD IHLI, B LD H 73 LD IHLI, B LD H 74 LD IHLI, B LD H 75 LD IHLI, B LD H 75 LD IHLI, B LD H 76 LD IHLI, B LD H 77 LD IHLI, B LD H 77 LD IHLI, B LD H 78 L	1	D28405	JP NC,NN	Н	118405	LD DE
E28405 JP PO.NN 58 LD E CA8405 JP Z.NN 59 LD E 382E JR C.DIS 5A LD E 1822 JR DIS 58 LD E 302E JR NC.DIS 5D LD E 202E JR NZ.DIS 5D LD E 202E JR NZ.DIS 5D LD E 202E JR Z.DIS 5D LD E 12 LD IBC.J 66 LD H 12 LD IBC.J 66 LD H 77 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B F 71 LD IHLI, B F 72 LD IHLI, B F 73 LD IHLI, B F 74 LD IHLI, B F 75 LD IHLI, B F 75 LD IHLI, B F 76 LD IHLI, B F 77 LD IHLI, B F 78 LD IHLI, B F 79 LD IHLI, B F 79 LD IHLI, B F 70 LD IHLI, B LD H 71 LD IHLI, B LD H 72 LD IHLI, B LD H 73 LD IHLI, B LD H 74 LD IHLI, B LD H 75 LD IHLI, B LD H 75 LD IHLI, B LD H 76 LD IHLI, B LD H 77 LD IHLI, B LD H 77 LD IHLI, B LD H 78 L	ı	C28405	JP NZ,NN		DD5E05	LD E,(I
282E JR Z.DIS 1E20 LD E 02 LD IBCI.A 66 LD H 12 LD IDEI.A 70 LD E 77 LD IHLI.A 7D6605 LD H 70 LD IHLI.B F 71 LD IHLI.C 60 LD H 72 LD IHLI.C 60 LD H 73 LD IHLI.C 60 LD H 74 LD IHLI.E 62 LD H 75 LD IHLI.E 62 LD H 76 LD IHLI.E 64 LD H 3620 LD IHLI.L 66 LD H 3620 LD IHLI.N 65 LD H 3620 LD IHLI.N 65 LD H	ı	EA8405	JP PE NN		FD5E05	LD E,(I
282E JR Z.DIS 1E20 LD E 02 LD IBCI.A 66 LD H 12 LD IDEI.A 70 LD E 77 LD IHLI.A 7D6605 LD H 70 LD IHLI.B F 71 LD IHLI.C 60 LD H 72 LD IHLI.C 60 LD H 73 LD IHLI.C 60 LD H 74 LD IHLI.E 62 LD H 75 LD IHLI.E 62 LD H 76 LD IHLI.E 64 LD H 3620 LD IHLI.L 66 LD H 3620 LD IHLI.N 65 LD H 3620 LD IHLI.N 65 LD H	ı	E28405 CA8405	JP PO,NN JP Z NN			LD E,B
282E JR Z.DIS 1E20 LD E 02 LD IBCI.A 66 LD H 12 LD IDEI.A 70 LD E 77 LD IHLI.A 7D6605 LD H 70 LD IHLI.B F 71 LD IHLI.C 60 LD H 72 LD IHLI.C 60 LD H 73 LD IHLI.C 60 LD H 74 LD IHLI.E 62 LD H 75 LD IHLI.E 62 LD H 76 LD IHLI.E 64 LD H 3620 LD IHLI.L 66 LD H 3620 LD IHLI.N 65 LD H 3620 LD IHLI.N 65 LD H	1	382E	JR C,DIS		5A	LD E,D
282E JR Z.DIS 1E20 LD E 02 LD IBCI.A 66 LD H 12 LD IDEI.A 70 LD E 77 LD IHLI.A 7D6605 LD H 70 LD IHLI.B F 71 LD IHLI.C 60 LD H 72 LD IHLI.C 60 LD H 73 LD IHLI.C 60 LD H 74 LD IHLI.E 62 LD H 75 LD IHLI.E 62 LD H 76 LD IHLI.E 64 LD H 3620 LD IHLI.L 66 LD H 3620 LD IHLI.N 65 LD H 3620 LD IHLI.N 65 LD H	1	302E	JR NC,DIS	Ш	5C	LD E,E
02 L0 (BC) A 56 LD H 12 L0 (DE) A DD6605 LD H 77 LD (HL) B 67 LD H 70 LD (HL) B 67 LD H 72 LD (HL) C 60 LD H 73 LD (HL) C 60 LD H 74 LD (HL) C 62 LD H 75 LD (HL) C 63 LD H 75 LD (HL) C 64 LD H 75 LD (HL) C 64 LD H 3620 LD (HL) C 64 LD H	1	282E	JR Z.DIS		5D 1E20	LD E,E
74 LD (HL),H 63 LD H 75 LD (HL),L 64 LD H 3620 LD (HL),N 65 LD H DD7705 LD (JX+d) A 3620 LD H	1	02 12	LD (BC),A LD (DE).A		66	LD H (I
74 LD (HL),H 63 LD H 75 LD (HL),L 64 LD H 3620 LD (HL),N 65 LD H DD7705 LD (JX+d) A 3620 LD H	ĺ	77	LD (HL),A		FD6605	LD H,(I
74 LD (HL),H 63 LD H 75 LD (HL),L 64 LD H 3620 LD (HL),N 65 LD H DD7705 LD (JX+d) A 3620 LD H	I	71	LD (HL) C		60	LD H,A
75 LD (HL),L 64 LD H 3620 LD (HL),N 65 LD H DD7705 LD (IX+d) A 2620 LD H		73		H	61	LD H,C
3620 LD (HL),N 65 LD H DD7705 LD (IX+d),A 2620 LD H DD7005 LD (IX+d),B 2A8405 LD H DD7105 LD (IX+d),C 218405 LD H			LD (HL) H	H	63	LDHE
DD7105 LD (IX+d),B 2620 LD H DD7105 LD (IX+d),C 218405 LD H	I	3620	ID (HI) N		65	LD H,L
DD7105 LD (IX+d),C 218405 LD H		DD7005	LD (IX+d),B	H	2A8405	LD H,N
UU/200 LU (IX+d),U FD47 ID 1	I	DD7105 DD7205	LD (IX+d),C LD (IX+d),D		218405 ED47	LD HL

	OBJ CODE	SOURCE STATEMENT
1	DD7305 DD7405	LD (IX+d),E LD (IX+d),H
ı	DD7505	LD (IX+d).L
1	DD360520 FD7705	LD (IX+d),N LD (IY+d),A
1		LD (IY+d).B
	FD7105 FD7205 FD7305 FD7405	LD (IY+d),D
	FD7305 FD7405	LD (IY+d),E LD (IY+d),H
	FD7505 FD360520	LD (IY+d),L LD (IY+d),N
	328405	LD (NN),A
	ED438405 ED538405	LD (NN),BC LD (NN),DE
	228405 DD228405	LD (NN),HL
	DD228405 FD228405 ED738405	LD (NN),IY
ı	ΠΔ	LD (NN),SP LD A,(BC) LD A,(DE) LD A,(HL)
Ì	1A 7E	LD A,(DE) LD A,(HL)
	DD7E05 FD7E05 3A8405	LD A,(IX+d) LD A,(IY+d) LD A,(NN)
1	3A8405	LD A,(NN)
	7F 78	LD A,A LD A,B
	79 7A	LD A,C LD A,D
J	7B	LD A,E
	7C ED57	LD A,H LD A,I
J	7D 3E20	
	46	LD B,(HL)
	DD4605 FD4605	LD B,(IX+d) LD B,(IY+d)
	47 40	IDPP
		IDBC
	41 42 43	LD B,D LD B,E
	44 45	
	9620	LD B,N
	ED4B8405 018405	LD BC,(NN)
	4E DD4E05	LD B,H,NN LD B,L LD B,N LD BC,(NN) LD BC,NN LD C,(HL) LD C,(IX+d) LD C,(IY+d) LD C,A
	FD4E05	LD C,(IY+d)
	48	LDCB
ı	49 4A	LD C,C LD C,D
	4B 4C	LD C,E LD C,H LD C,L LD C,N
	4D	LD C,L
	0E20 56	LD C,N LD D,(HL)
	DD5605 FD5605	LD D,(HL) LD D,(IX+d) LD D,(IY+d)
	57 50	
	51	LD D.C
1	52 53	LD D,D LD D,E
i	54 55	LDD,H LDD,L
	1620	LDDN
ļ	ED588405 118405	LD DE,(NN) LD DE,NN
1	5E DD5E05 FD5E05	LD E (HL) LD E (IX+d)
	FD5E05 5F	LD DE,(NN) LD DE,NN LD E,(HL) LD E,(IX+d) LD E,(IY+d) LD E,(IY+d) LD E,A
	58	LD E.B
	59 5A	LD E.D
	5B 5C	LD E,E LD E,H
	5D	LD E,L
	1E20 66	LD E,E LD E,H LD E,L LD E,N LD H,(HL)
	DD6605 FD6605	LD H,(IX+d)
	67	LD H,A
1	60 61	LDHC
Į	62 63	LD H,D LD H,E
1	64 65	IDHH
	2620	LDHN
	2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	LD HL (NN)
1	2A8405 218405 ED47	LD HL,(NN) LD HL,NN LD 1,A

CODE	SOURCE STATEMENT
DD248405 DD218405	LD IX,(NN) LD IX,NN LD IY,(NN) LD IY,NN LD I,(HL) LD L,(IX+d) LD L,(IX+d)
DD218405 FD2A8405 FD218405	LD IY (NN)
GE DDGE05	LD L (IX+d)
FD6E05 6F	LD L,(IY+d) LD L,A
68 69	LD L.B LD L.C
6A 6B	LD L.C LD L.D LD L.E
6C 6D	LD L,H LD L,L
2E20 ED7B8405 F9	LD L,N LD SP,(NN)
F9 DDF9 FDF9	LD L,D LD L,E LD L,H LD L,L LD L,N LD SP,(NN) LD SP,HL LD SP,IX LD SP,IY LD SP,NN LD SP,NN
318405	LD SP,IY LD SP,NN
EDA8 EDB8	LDDR
EDA0 EDB0	LDI LDIR
ED44 00	LDI LDIR NEG NOP OR (HL) OR (IX+d)
B6 DDB605 FDB605	OR (HL) OR (IX+d)
B7	
80 B1	OR B OR C OR D
B2 B3	OR E
B4 B5	OR H OR L
F620 EDBB	OTOR
EDB3 ED79 ED41	OUT (C),A
ED41 ED49 ED51	OUT (C),B
ED59 ED61	OUT (C),E
ED69 D320 EDAB	OUT (C),L
EDAB EDA3	OR L OR N OTDR OTTR OUT (C),A OUT (C),C OUT (C),C OUT (C),C OUT (C),L OUT (C),L OUT (N),A OUTD OUTD OUTD OUTD OUTD OUTD OUTD OUTD
EDA3 F1 C1	POP AF POP BC
F1	POP DE POP HL
DDE1 FDE1	POP IX
F5 C5	PUSH AF PUSH BC
D5 E5	PUSH AF PUSH BC PUSH DE PUSH HL PUSH HX
DDE5 FDE5	PUSH HL PUSH IX PUSH IY RES 0,(HL) RES 0,(IX+d) RES 0,A RES 0,B
CB86 DDC80586	RES O,(HL) RES O,(IX+d)
DDC80586 FDC80586 CB87	RES O,(IY+d)
CB80 CB81	RES O.B RES O.C
CB82 CB83 CB84	RES O.C RES O.D RES O.E RES O.H
CB85 CB8E	DECOL
DDCB058E FDCB058E	RES 1,(HL) RES 1,(IX+d) RES 1,(IY+d) RES 1,A RES 1,B
CB8F CB88	RES 1,(IY+d) RES 1,A RES 1,B RES 1,C RES 1,D
CB89 CB8A	00010
CB8B CB8C	RES 1,E RES 1,H
CB8D CB96	RES 1,L RES 2 (HL)
DDCB0596 FDCB0596	RES 2 (HL) RES 2 (IX+d) RES 2 (IY+d)
■ CB97	RES 2,A RES 2,B
CB90 CB91 CB92	RES 2,C RES 2,D RES 2,E RES 2,H
CB93 CB94	RES 2,E RES 2,H
CB95 CB9E	RES 2,L RES 3,(HL)
DDCB059E FDCB059E CB9F	RES 1.D RES 1.H RES 2.(HL) RES 2.(IX +d) RES 2.(IX +d) RES 2.(IX +d) RES 2.D RES 2.D RES 2.D RES 2.D RES 2.L RES 2.L RES 3.(IX +d) RES 3.(IX +d) RES 3.(IX +d) RES 3.A
CB9F CB98	RES 3,A RES 3,B

CODE STATEMENT  CB93 RES 3.D CB98 RES 3.D CB98 RES 3.D CB98 RES 3.D CB98 RES 3.L CB90 RES 4.L CB90 RES 4.L CB90 RES 4.L CB90 RES 5.L CB90 RES 7.L CB	BJ .	SOURCE	ז ר
C89A RES 3,D C89A RES 3,E C89C RES 3,H C89C RES 3,H C89C RES 3,H C89A RES 4,E C89A RES 4,(IX+d) FDC805A6 RES 4,(IX+d) FDC805A6 RES 4,(IX+d) FDC805A6 RES 4,IX+d) RES 4,B C8A4 RES 5,B C8A4 RES 5,IX+d) FDC805A6 RES 5,(IX+d) FDC805A6 RES 5,IX+d) FDC805A6 RES 6,IX+d) FDC805A6 RES 7,IX+d) FDC805A6			1 1
C89B RES 3,E C89B RES 3,E C89C RES 3,L C89D RES 3,L C89D RES 3,L C89D RES 4,HL) DOCBOSAG RES 4,HL) DOCBOSAG RES 4,HL) C8A1 RES 4,C C8A2 RES 4,C C8A2 RES 4,C C8A3 RES 4,E C8A4 RES 4,L C8A5 RES 4,L C8A6 RES 5,L C8A6 RES 5,HV-d) DOCBOSAG RES 5,HV-d) DOCBOSAG RES 5,HV-d) DOCBOSAG RES 5,HV-d) C8A6 RES 5,HV-d) C8A7 RES 5,HV-d) C8A8 RES 5,D C8A8 RES 5,D C8A8 RES 5,D C8A8 RES 5,C C8AA RES 5,D C8AA RES 5,D C8AB RES 5,C C8AA RES 5,D C8BA RES 5,C C8AA RES 5,D C8BA RES 5,C C8AA RES 5,D C8BA RES 5,C C8AB RES 5,C C8AB RES 5,C C8AB RES 5,C C8AB RES 5,H DOCBOSAG RES 6,HL DOCBOSAG RES 6,HL DOCBOSAG RES 6,HL DOCBOSAG RES 6,HL C8BT RES 6,D C8BT RES 6,D C8BT RES 6,D C8BT RES 7,HL DOCBOSAG RES 7,HL C8BT RES 7,D C8BT RES 7,H C8BT RES 8,H C8BT RES 7,H C8B		RES 3,C	113
C89D RES 3,L C8A6 RES 4,(IN-t) DDCB05A6 RES 4,(IN-t) FDCB05A6 RES 4,(IN-t) RES 4,(IN-t) RES 4, IN-t) RES 5, IN-t) RES 6, IN-t) RES 7, I	9B	RES 3,E	11
CBA6		RES 3,H RES 3 1	113
FDCB05AB RES 4, (I) Y-td) CBA7 CBA0 RES 4, B CBA1 RES 4, C CBA2 RES 4, C CBA2 RES 4, C CBA2 RES 4, C CBA3 RES 4, E CBA4 RES 4, L CBA5 RES 5, E CBA4 RES 5, I, L CBA5 RES 5, I, IX-td) FDCB05AE RES 5, I, IX-td) FDCB05AE RES 5, I, IX-td) FDCB05AE RES 5, I, IX-td) RES 6, IX-td) RES 7, IX	A6	RES 4,(HL)	111
CBA7 CBA0 CBA1 RES 4,B CBA1 RES 4,C CBA2 RES 4,C CBA2 RES 4,C CBA3 RES 4,C CBA3 RES 4,C CBA4 RES 4,L CBA5 RES 5,E RES 5,(IN-t-d) RES 6,(IN-t-d) RES 7,(IN-t-d) RES 6,(IN-t-d) RES 6,(IN-t-d) RES 7,(IN-t-d) RES 7,(IN-t-	CB05A6 CB05A6		11:
CBA1 RES 4,C CBA2 RES 4,D CBA3 RES 4,E CBA4 RES 4,H CBA5 RES 5,E RES 5,IR1-10 DDCB05AE RES 5,IR1-10 FDCB05AE RES 5,IR1-10 CBAF RES 6,IR1-10 CBAF RES 6,IR1-1	A7	RES 4 A	
CBA2 RES 4,0 CBA3 RES 4,E CBA4 RES 4,L CBA5 RES 5,E CBA6 RES 5,IHL) DDCB05AE RES 5,IIX-d FDCB05AE RES 5,IIX-d FDCB05AE RES 5,IX-d CBA7 RES 5,B CBA9 RES 5,B CBA9 RES 5,B CBA9 RES 5,B CBA0 RES 5,C CBAA RES 5,C CBAB RES 5,B CBA0 RES 5,C CBAB RES 5,C CBAB RES 6,IX-d CBB6 RES 6,IX-d CBB6 RES 6,IX-d CBB7 RES 6,IX-d CBB7 RES 6,IX-d CBB7 RES 6,IX-d CBB8 RES 7,IX-d CBB8 RE		RES 4,B	113
CBA4 RES 4,H CBA5 RES 5,ILL DDCB05AE RES 5,ILX dI RES 5,IIX dI RES 6,IIX dI RES 6,IIX dI RES 6,IIX dI PDCB05BE RES 6,IIX dI RES 6,IIX d	A2	RES 4,D	11
CBA5  SPACE	A3 A4		11
DDCB05AE RES 5,(IX'+d) FDCB05AE RES 5,(IX'+d) FDCB05AE RES 5,(IX'+d) RES 5, IX'+d) RES 5, IX'+d) RES 6, IX + GES 6, IX'+d) RES 7, IX'+d)	A5	RE\$ 4,L	11:
CBAF CBAS CBAS CBAS CBAS CBAS CBAS CBAS CBAS		RES 5,(HL)	ш
CBA8 RES 5,B CBA9 RES 5,C CBAA RES 5,C CBAA RES 5,C CBAA RES 5,C CBAB RES 5,E CBAC RES 5,H CBBD RES 6,(HX-d) RES 6,(HX-d) RES 6,(HX-d) RES 6,(HX-d) RES 6,(HX-d) RES 6,(HX-d) RES 6,C CBB1 RES 6,E CBB1 RES 6,E CBB2 RES 6,E CBB3 RES 7,(HX-d) PDCB05BE RES 7,(HX-d) PDCB05BE RES 7,(HX-d) PDCB05BE RES 7,(HX-d) RES 7,E CBBC	CB05AE	RES 5,(1Y+d)	11:
CBA9 CBA9 CBA9 CBA6 CBA6 CBA6 RES 5, C CBAC RES 5, C CBAC RES 5, C CBAC RES 5, C RES 6, C RES 6, C RES 6, C CBB7 RES 7, C CBB8 RES 7, C CBB7 RES 7, C CBB8 RES 7, C CBB8 RES 7, C CBB8 RES 7, C CBB8 RES 7, C CBB7 RES 7, C CBB8 RES 7, C CBB8 RES 7, C CBB8 RES 7, C CBB7 RES 7, C CBB8 RES 7, C CBB9 R		RES 5.B	
CBAB RES 5,E CBAC RES 5,H CBAD RES 5,L CBBO RES 6,(IN-d) DDCB05666 RES 6,(IX-d) CBB1 RES 6,C CBB1 RES 6,C CBB1 RES 6,C CBB2 RES 6,C CBB2 RES 6,C CBB2 RES 6,C CBB3 RES 6,C CBB3 RES 6,C CBB4 RES 6,L CBB4 RES 6,L CBB5 RES 7,IIX-d) FDCB05666 RES 7,IIX-d) FDCB05676 RES 7,IIX-d) FDCB0576 RES 7,IIX-d) FDCB0577 RES		RES 5,C	11:
CBAC RES 5,H CBAD RES 6,L CBAD RES 6,L CBBO RES 6,B CBBO RES 6,L CBBO RES 7,L CBBO		RES 5,E	11
CBB6 RES 6,(HL) DDCB0586 RES 6,(IX-d) FDCB0586 RES 6,(IX-d) FDCB0586 RES 6,(IX-d) CBB1 RES 6,0 CBB2 RES 6,0 CBB2 RES 6,0 CBB3 RES 6,0 CBB3 RES 6,0 CBB3 RES 6,0 CBB3 RES 6,1 CBB6 RES 7,(IX-d) FDCB0586 RES 7,(IX-d) FDCB0586 RES 7,(IX-d) FDCB0587 RES 7,(IX-d) FDCB0587 RES 7,(IX-d) CBB8 RES 7,0 CBB RES		RES 5,H	113
DDC80586 RES 6, (IX+d) FDC80586 RES 6, (IX+d) RES 6, ARS 7, ARS 6, ARS 7, ARS 6, ARS 7, ARS 7	B6	RES 6,(HL)	
CBB7 RES 6, A CBB1 RES 6, B CBB1 RES 6, C CBB2 RES 6, C CBB3 RES 6, C CBB3 RES 6, C CBB3 RES 6, C CBB4 RES 6, C CBB4 RES 7, C CBB6 RES 7, C CBB6 RES 7, C CBB7 RES 7, C CBB8 RES 7, C CBB0 RES 7, C CB	CB05B6	RES 6.(IX+d)	11:
CBB2 RES 6,D CBB3 RES 6,E CBB4 RES 6,E CBB5 RES 6,L CBB6 RES 7,(IN1-d) DDCB05BE RES 7,(IN1-d) FDCB05BE RES 7,(IN1-d) FDCB05BE RES 7,IN1-d) CBB6 RES 7,E CBB7 RES 7,E CBB8 RES 7,E CBB RET C CBB RET C RET M CB RET M C	B7	RES 6,A	1 1 1
CBB2 RES 6,D CBB3 RES 6,E CBB4 RES 6,E CBB4 RES 6,E CBB5 RES 7,(IN:-d) DDCB05BE RES 7,(IN:-d) FDCB05BF RES 7,(IX:-d) FDCB05BF RES 7,(IX:-d) FDCB05BF RES 7,(IX:-d) FDCB05BF RES 7,E CBBB RES 7,E CBBC RES 7,I CBB RET C CBB RET C CBB RET C CBB RET C CBB RET PE E0 RET NO E0 RET PE E0 RET NO E0 RET PE E0 RET NO E0 RE	B0 R1	RES 6,B	11:
CBB4 RES 6, H CBB5 RES 7, (HL) DDCB05BE RES 7, (HX-d) FDCB05BE RES 7, (HX-d) FDCB05BE RES 7, (HX-d) CBB6 RES 7, (HX-d) CBB7 RES 7, (HX-d) RES	B2	RES 6,D	11
CBB5 RES 6,L CBB6 RES 7,INL-I DDCB05BE RES 7,INL-I FDCB05BE RES 7,INL-I CBB7 RES 7,INL-I CBB8 RES 7,B CBB9 RES 7,C CBB8 RES 7,C CBB8 RES 7,C CBB8 RES 7,C CBB8 RES 7,C CBB0 RET NC CC CBB0 RET NC CC C	B4	RES 6,H	11:
DDC8058E RES 7, (IX'-d) FDC8058F RES 7, (IX'-d) CB88 RES 7, A CB89 RES 7, C CB80 RES 7	B5	RES 6,L	11
CBBB RES 7,A CBBB RES 7,B CBBB RES 7,C CBBC RES 7,L CBC RE	CB05BE	RE\$ 7,(IX+d)	11:
CBBB HES 7,D CBBC RES 7,E CBC R		RES 7,(1Y+d)	11:
CBBB HES 7,D CBBC RES 7,E CBC R	B8	RES 7,B	11
CBBB RES 7, E CBBC RES 7, E CBBD RES 7, E CB RET C FB RET C FB RET TM DO RET NC CO RET NZ FO RET P EB			113
CBBD RES 7,L CP RET DB RET C FB RET C FB RET T M DO RET NC CO RET NZ FO RET P EB RET PE EB RET P	88	RES 7,E	11
C9 RET C D8 RET C P8 RET M D0 RET NC C C RET NC C C RET NC C C RET NC C RET		RES 7,H RES 7 L	Ш
F8 RET M D0 RET NC C0 RET NC F0 RET P E8 RET PE E0 RET P E0 RET V E045 RETN C816 RL (HL) DDC80516 RL (HX+d) FDC80516 RL (HX+d) C817 RL A C818 RL A C818 RL A C811 RL C C812 RL D C813 RL E C814 RL C C815 RL L 17 RL A C806 RLC (HX+d) FDC80506 RLC (HX+d) FDC8051E RLC C C801 RLC C C802 RLC D C803 RLC B C804 RLC C C805 RLC L O7 RLC A C806 RLC H C807 RLC C C808 RLC C C808 RLC C C809 RLC C C809 RLC C C801 RLC C C801 RLC C C801 RLC C C802 RLC C C803 RLC C C804 RLC C C805 RLC C C806 RLC C C806 RLC C C807 RLC C C808 RLC C C808 RLC C C809 RLC C C801 RLC C C801 RLC C C801 RLC C C801 RLC C C802 RLC C C803 RLC C C804 RLC C C805 RLC C C806 RLC C C807 RLC C C808 RLC C C808 RLC C C809 RLC C R		RET	11
DO RET NC CO RET NC CO RET P E8 RET PE E8 RET PE E0 RET PO C8 RET I E045 RET I E045 RET I E046 RET I E046 RET I E046 RET I E047 RET I E047 RET I E047 RET I E048 RET		RETM	11:
F0 RET P E8 RET PE E8 RET PE E9 RET PE E0 RET I E045 RET I E045 RET I E046 RET I E046 RET I E046 RET I E047 RET I RL (IX+d) FDC80516 RL (IX+d) FDC80516 RL C E811 RL C C812 RL D C812 RL D C813 RL E C814 RL H C815 RL L 17 RLA C816 RLC (IX+d) FDC80506 RLC (IX+d) FDC80506 RLC (IX+d) FDC80506 RLC (IX+d) E0607 RLC A C8007 RLC B E0607 RLC C E0604 RLC L E0607 RLC B E0607		RET NC	11:
C8 RET Z ED4D RET1 ED45 RETN C816 RL (HL) DDC80516 RL (IX+d) FDC80516 RL (IX+d) C817 RL A C810 RL B C811 RL C C812 RL D C813 RL E C814 RL H C815 RL L 17 RLA C806 RLC (IX+d) FDC80506 RLC (IX+d) FDC8051E RLC D C803 RLC D C804 RLC D C805 RLC D C805 RLC D C806 RLC RR (IX-d) FDC8051E RR (IX-d) FDC		RETP	
C8 RET Z ED4D RET1 ED45 RETN C816 RL (HL) DDC80516 RL (IX+d) FDC80516 RL (IX+d) C817 RL A C810 RL B C811 RL C C812 RL D C813 RL E C814 RL H C815 RL L 17 RLA C806 RLC (IX+d) FDC80506 RLC (IX+d) FDC8051E RLC D C803 RLC D C804 RLC D C805 RLC D C805 RLC D C806 RLC RR (IX-d) FDC8051E RR (IX-d) FDC		RET PE	111
C816 RL (IHL) DDC80516 RL (IV+d) FDC80516 RL (IV+d) C817 RL A C810 RL B C812 RL D C813 RL E C814 RL H C815 RL C C806 RLC (IX+d) FDC80506 RLC (IX+d) FDC80506 RLC (IX+d) C807 RLC A C800 RLC B C800 RLC B C800 RLC C C800 RLC B C800 RLC C C800 RLC B C800 RLC C C800 RLC		RETZ	11
C816 RL (IHL) DDC80516 RL (IV+d) FDC80516 RL (IV+d) C817 RL A C810 RL B C812 RL D C813 RL E C814 RL H C815 RL L 17 RLA C806 RLC (IX+d) FDC80506 RLC (IX+d) FDC80506 RLC (IX+d) FDC80506 RLC (IX+d) C807 RLC A C800 RLC B C800 RLC C C800 RLC B C800 RLC C C80		RETN	11:
FDC89516 RL (IV+d) C811 RL B C812 RL D C812 RL D C813 RL E C814 RL H C816 RL C (IX+d) FDC89506 RLC (IX+d) FDC89506 RLC (IX+d) C800 RLC B C800 RLC C C800 R		RL (HL)	11
CB10 RL B CB12 RL D CB12 RL D CB13 RL E CB14 RL H CB15 RL L 17 RLA CB06 RLC (HL) DDC80506 RLC (IX+d) FDC80506 RLC (IX+d) FDC80506 RLC (IX+d) CB07 RLC A CB00 RLC C CB03 RLC C CB03 RLC C CB03 RLC C CB04 RLC C CB05 RLC C CB05 RLC C CB06 RLC C CB07 RLC C CB08 RLC C CB08 RLC C CB08 RLC C CB09 RLC C CB	CB0516	RL (IY+d)	Ш
CB11 RL C CB12 RL D CB13 RL E CB14 RL H CB15 RL L 17 RLA RL (IK) DDC80506 RLC (IX) FDC80506 RLC (IX) CB07 RLC (IX) CB07 RLC CB CB01 RLC C CB02 RLC D CB02 RLC D CB03 RLC B CB04 RLC H CB05 RLC H CB05 RLC B CB06 RLC B CB06 RLC B CB07 RLC B CB08 RLC B CB18 RR B CB18 RR B CB19 RR C CB18 RR B CB10 RR C CB10 RR C CB10 RR C CB11 RR B CB10 RR C CB10	17		11:
CB13 RL E CB14 RL H CB15 RL L 17 RLA CB06 RLC (IL) DDC80506 RLC (IX+d) FDC80506 RLC (IY+d) CB07 RLC B CB01 RLC C CB02 RLC D CB03 RLC D CB03 RLC B CB04 RLC H CB05 RLC L 07 RLCA CB06 RLC B CB16 RR (IL) CB16 RR (IX+d) FDC8051E RR (IX+d) CB18 RR B CB19 RR C CB19 RR C CB14 RR D CB16 RR L CB17 RR A CB18 RR B CB19 RR C CB19 RR C CB10 RR L CB10 RR R L CB10 RR R R R CB10 RR R R CB10 RR R R R CR R R R R R CR R R R R R CR R R R	11	RL C	11
CB15 RL L 17 RLA RLA CB06 RLC (HL) DDC80506 RLC (IY+d) FDC80506 RLC (IY+d) CB07 RLC B CB01 RLC C CB02 RLC D CB03 RLC C CB03 RLC C CB04 RLC C CB05 RLC L 07 RLCA CB06 RLC C CB07 RLC C CB08 RLC C CB16 RR (IL) CB16 RR (IX-d) CB17 RR A CB18 RR B CB18 RR B CB19 RR C CB14 RR D CB16 RR C CB17 RR C CB18 RR B CB19 RR C CB19 RR C CB10 RR C CB10 RR C CB10 RR C CB11 RR C CR	12 13	RL D BL E	
17 RLA CB06 RLC (IX+d) DDC80506 RLC (IX+d) FDC80506 RLC (IX+d) CB07 RLC A CB00 RLC B CB01 RLC C CB02 RLC D CB03 RLC C CB03 RLC C CB04 RLC C CB04 RLC C CB05 RLC C CB06 RLC C CB06 RLC C CB07 RLC C CB08 RLC C R	14	RL H	11
C806 RLC (HL) DDC80506 RLC (IY+d) FDC80506 RLC (IY+d) C807 RLC A C800 RLC B C801 RLC C C802 RLC D C803 RLC C C803 RLC C C804 RLC H C805 RLC L 07 RLC A C806 RLC B C806 RLC L 07 RLC C C807 RLC C C808 RLC L 07 RLC C C808 RLC L 08 RR (HL) DDC8051E RR (IY+d) C818 RR B C818 RR B C818 RR B C819 RR C C814 RR D C816 RR H C810 RR H C810 RR H C810 RR H C810 RR L C811 RR L C811 RR L C811 RR L C811 RR L C812 RR C C813 RR C C814 RR D C815 RR L C816 RR L C817 RR L C818 RR B C819 RR C C810 RR R C C810 RR C	15	RLA	
FDCB0506 RLC (IY+d) CB00 RLC B CB01 RLC C CB02 RLC D CB03 RLC D CB03 RLC C CB04 RLC H CB05 RLC L 07 RLCA ED6F RLD CB1E RR (IK-d) FDCB051E RR (IY-d) CB1F RR A CB1B RR B CB1B RR B CB1B RR C CB1A RR D CB1B RR C CB1B RR B CB1B RR B CB1B RR B CB1B RR C CB1B RR		RLC (HL)	11:
CB07 RLC A CB00 RLC B CB01 RLC C CB02 RLC D CB03 RLC E CB04 RLC H CB05 RLC L 07 RLCA CD1E RR (HL) DDCB051E RR (IX'd) FDCB051E RR (IX'd) FDCB051E RR (IX'd) CB18 RR B CB18 RR B CB19 RR C CB14 RR D CB16 RR L CB17 RR C CB18 RR B CB19 RR C C	CB0506	RLC (IY+d)	
C801 RLC C C802 RLC D C803 RLC E C804 RLC H C805 RLC L O7 RLCA ED6F RLD C81E RR (HL) DDC8051E RR ((X+d) FDC8051E RR ((X+d) C818 RR B C818 RR B C819 RR C C814 RR D C816 RR E C817 RR C C818 RR C C818 RR C C819 RR C C81		RLC A	
C802 RLC D C803 RLC D C804 RLC H C805 RLC L 07 RLCA ED6F RLD C81E RR (HL) DDC8051E RR (IY-d) C81F RR A C818 RR B C819 RR C C81A RR D C81A RR D C81C RR H C81C RR C C81C RR H C81C RR H C81C RR H C81C RR C C C81C RR C C C81C RR C C C81C RR C C C81C	01	RLC C	11
C804 RLC H C805 RLC L 07 RLCA ED6F RLD C81E RR (HL) DDC8051E RR (IY-d) C81F RR A C818 RR B C819 RR C C81A RR D C81B RR E C81C RR H C81C RR C C C81C RR C C81C RR C C C81C RR C C C81C RR C C C81C RR	02 03	RLCD	
07 RLCA ED6F RLD CB1E RR (HL) DDCB051E RR (IY-d) FDCB051E RR (IY-d) CB1F RR A CB1B RR B CB1B RR C CB1A RR D CB1A RR D CB1C RR H CB1D RR L TF RRA CB0E RR C GB1 RR L TF RRA CB0E RRC (HL)	04	RLC H	
ED6F RLD CB1E RR (HL) DDCB051E RR (IX+d) FDCB051E RR (IX+d) CB1F RR A CB18 RR B CB19 RR C CB1A RR D CB1B RR E CB1C RR H CB1D RR L CB1D RR L CB1D RR L CB1D RR L CB1 RR A CB1D RR C CB1 RR CB1D		RLCA	
DDCB051E RR (IX-4) FDCB051E RR (IY-4) CB1F RR A CB19 RR C CB19 RR C CB1A RR D CB1B RR E CB1C RR H CB1D RR L CB1C RR L CB1C RR L CB1C RR L CB1C RR C	6F	RLD	11
FDCB051E RR (IY'd) CB1F RR A CB1B RR B CB19 RR C CB1A RR D CB1A RR D CB1C RR H CB1C RR H CB1C RR L CB1 RR C (HL)	CB051E	RR (IX+d)	
CB19 RR C CB1A RR D CB1B RR E CB1C RR H CB1D RR L 1F RRA CB0E RRC (HL)	CB051E	RR (IY+d)	11
CB1A RR D CB1B RR E CB1C RR H CB1D RR L 1F RRA CB0E RRC (HL)	18	RRB	
CB18 RR E CB1C RR H CB1D RR L 1F RRA CB0E RRC (HL)	19 14	RR C RR D	11.
CB1D RR L 1F RRA CB0E RRC (HL)	18	RRE	
1F RRA CB0E RRC (HL)			11
		RRA	
		RRC (HL) RRC (IX+d)	11
FDCB050E RRC (IY+d) CB0F RRC A	CB050E	RRC (LV+d)	11
CB08 RRCB	08	RRCB	
CB09 RRC C	09	RRC C	JL

OBJ CODE	SOURCE STATEMENT	Ì
		ı
CBOA CBOB	RRC D RRC E	ı
CB0C	RRC H	ı
CB0D	RRCL	ı
OF ED67	RRCA 1	ı
C.7	RST 0	ı
D7 DF	RST 10H RST 18H	ı
DF 57	RST 18H	ı
E7 EF	RST 28H	l
F7	RRD RST 0 RST 10H RST 18H RST 20H RST 28H RST 30H RST 38H	
FF CF		ı
9E	SBC A,(HL)	
DD9E05 FD9E05	SBC A,(IX+d) SBC A,(IX+d)	
9F	SBC A.A	
98	SBC A,B	
99 9A	SBC A,C SBC A,D	
9B	SBCAE	
9C 9D	SBC A,H SBC A,L	
DF20	SBC A,N SBC HL,BC SBC HL,DE SBC HL,HL SBC HL,SP	
ED42	SBC HL,BC	
ED62 ED62 ED72	SBC HL,DE	
ED72	SBC HL,SP	
27		
CBC6 DDCB05C6 FDCB05C6 CBC7 CBC0	SET 0.(HL) SET 0.(IX+d) SET 0.(IY+d) SET 0.A	l
FDCB05C6	SET O,(IY+d)	l
CBC/	SET O,A SET O,B	ı
CBC1	SET O,C SET O,D	
CBC2 CBC3 CBC4	SET 0,B SET 0,C SET 0,D SET 0,E SET 0,H	
CBC3	SET O.E SET O.H	l
CBC5	SET O,L SET 1,(HL)	
CBCE	SET 1 (IVAN)	
DDCB05CE FDCB05CE	SET 1,(IY+d)	
CBCF CBC8	SET 0,H SET 0,L SET 1,(HL) SET 1,(IX+d) SET 1,IX+d) SET 1,A SET 1,B	Ì
CBC9		
CBCA		
CBCB	SET 1.E	ŀ
CBCD	SET 1,L SET 2 (HL) SET 2 (IX+d) SET 2 (IY+d)	
CBD6	SET 2 (HL)	
DDCB05D6 FDCB05D6	SET 2.(IY+d)	ı
CBD7 CBD0		
CBD0		l
CBD1 CBD2 CBD3 CBD4	SET 2,D	
CBD3	SET 2,E SET 2,H	
CBD5	SET 2,L SET 3,B	ı
CBD8	SET 3,B	ı
DDCB05DE FDCB05DE	SET 3,(HL) SET 3,(IX+d) SET 3,(IY+d) SET 3,A SET 3,C	ı
FDCB05DE	SET 3 (IY+d)	ı
CBDF CBD9	SET 3,A SET 3 C	ı
CBDA	SET 3,D SET 3,E SET 3,H	ı
CBDB	SET 3,E	ı
CBDD		ı
CBE6	SET 3,H SET 3,L SET 4,(H/L) SET 4,(1X+d) SET 4,(1Y+d) SET 4 A	ı
DDCB05E6 FDCB05E6 CBE7	SET 4 (1Y+d)	i
CBE7		
CBE0 CBE1	SET 4,B SET 4,C	ŀ
		ŀ
CBE 3	SE14,E (	ı
CBE4 CBE5	SET 4,H SET 4.1	ı
CBEE		ı
DDC805EE FDCB05EE	SET 5,(IX+d) SET 5,(IX+d)	ı
CBEF		ı
CBE8 CBE9	SET 5,B	ĺ
CBEA	SET 5,C SET 5,D	ĺ
CBEB	SET 5,E	ĺ
CBEC	SET 5 H SET 5 L	ı
CREG		ĺ
DDCB05F6 FDCB05F6	CET & (IVAA)	ĺ
CBF7	SET 6 A	ĺ
CBF0 CBF1	SET 6,B SET 6,C	ĺ
LOFI	3E   0,L	ı

#### **APPENDIX C**

# HARDWARE EXPANSION (S-100 Bus)

By now you have become familiar with the Sorcerer and have begun exploring its software. You will also want to investigate the many available hardware options. We will mention a few of these, to give you an idea of what is available.

#### **Exidy S-100 Expansion Unit**

To keep your Sorcerer up to date in the ever changing world of microcomputers, Exidy offers an S-100 Expansion Unit. Over 300 manufacturers produce S-100 devices ranging from floppy disk units to music synthesizers to color graphics boards; these are all available to the Sorcerer through the Expansion Unit. Our S-100 unit plugs right into the Sorcerer's 50-pin edge connector. It has its own 12 amp power supply and a mother board with six slots (2 amps per slot). The units can be daisy-chained if more slots are needed.

The S-100 Expansion Unit will support devices which do Direct Memory Access (DMA). S-100 plug-in cards are assigned to blocks of memory addresses or to Z80 I/O ports by means of jumper pads or DIP switches built into the cards. The Expansion Unit bus control circuits allow you to assign an S-100 card to any block of addresses or I/O port not currently used by the Sorcerer.

# Exidy I/O Expansion Kit (not needed with the Exidy S-100 Expansion Unit)

The I/O Expansion Kit contains an S-100 interface card and a cable which attaches the card to the Sorcerer's 50-pin edge connector. The interface card has the same bus controllers and buffers as the Exidy S-100 Expansion Unit; it fits into any other S-100 mother board and allows the Sorcerer to control the bus.

# Sorcerer Memory Expansion Kit

This kit conains eight 16K RAMs — enough to increase the Sorcerer's internal memory from 8K to 16K, or from 16K to 32K. To increase memory from 8K to 32K, two kits are needed.

## **Exidy Dual Disk Drive**

Our dual disk unit has all the capacity of an 8-inch floppy in a 5½ inch format. The drive is quad-density with 630,000 bytes on line, easily expandable to over a million bytes. Each drive has 77 tracks of 16 sectors, with 256 bytes per sector, for a total of 315K bytes per drive. Track-to-track positioning time is only 30 milliseconds, and the data transfer rate is 250,000 baud.

The Exidy Dual Disk Drive contains an S-100 controller card, cables, documentation, Disk Operating System software, an Extended Disk BASIC, and an Assembler.

### **Exidy Line Printer**

The Exidy Line Printer prints five-by-seven dot matrix characters with a throughput of up to 90 lines per minute, in formats of 80 to 132 columns; vertical spacing is six lines per inch. Paper tractors on each side of the carriage provide positive feed. The printer takes a cartridge ribbon, so ribbon changes are clean and simple.

### **Exidy Graphic Printer**

The Exidy EX-820 Graphics Printer prints graphics or standard ASCII characters on five-inch wide electrosensitive paper. Three character sizes (80, 40, and 20 columns) and three graphic resolutions (512, 256, and 128 dots) are software selectable. Software commands also shift the printer between the graphic and alphanumeric modes and between standard and reverse printing.

The graphics printer accepts serial or parallel data at 300 or 1200 baud (jumper selectable). Data cables are not included.

# APPENDIX D

#### **GLOSSARY OF TERMS**

A glossary of computer buzz words will help you understand some of the text you will encounter on your personal computing venture.

**ASCII** — Acronym for American Standard Code of Information Interchange. Refers to computer industry standardization of computer binary representation of letters, numbers, symbols and certain control characters, so that information can easily be transferred from one computer or peripheral device to another.

**Assembler** — Large machine-language program that translates human coded instructions into binary information the computer can use. Assembly Language instructions are abbreviated English commands (called mnemonic codes) that can be easily memorized and written by the computer user. Assembly Language programming is fast and concise but somewhat cryptic to read.

**Banked Memory** — A method of enlarging the usual 64K RAM memory space addressable by 8-bit microprocessors to a much larger range, usually ½ to 1 megabyte. To avoid addressing confusion, boards above 64K are switched on only when needed by software control.

**Baud** — A term used to define the signalling speed of information in a computer (typically relating to input and output). It is the number of bits of info per second.

**Binary** — The two-digit (bit) number system based on 0 and 1. Gates are electronic circuits that are either on or off; these two states can represent the binary bits 0 and 1.

**Bit** — A binary digit (0 or 1).

**Bootstrap** — A method by which a computer awakens to full awareness once it is turned on. Usually refers to PROM software that initiates this awakening.

**Bubble Memory** — Computer memory that does not forget what it knows, even when the power is shut off. Bubble memory is mid-way in price and speed between PROM and floppy disk.

**Bug** — An error in the hardware **or** software of a computer.

<b>Bus</b> — A power or communication line used in common by many parts of a computer. The S-100 Bus is 100 such parallel common communication lines, each of which is capable of carrying one bit or signal through the entire computer.
Byte — A string of eight binary bits.
<b>Call</b> — Any request by a user's program for executive action by the computer Monitor or Operating System.
<b>Central Processing Unit</b> — The nerve center of a computer; the network of electronic circuits that interprets programs and tells a computer how to carry them out.
$ \begin{array}{l} \textbf{Chip} - \text{Computer jargon for the tiny silicon slices used to make electronic memories and other circuits. A single chip may have as many as 30,000 electronic parts! \end{array} $
<b>Circuit Board</b> — A rigid fiberglass or phenolic card upon which various electronic parts are mounted. Printed or etched copper tracks connect the various parts to one another.
<b>Clock</b> — The master timing circuit for a microprocessor that synchronizes all its operations. Some microprocessors have tuneable clocks so that its operational speed can be increased or decreased by the user for a particular application.
Code — See Program.
<b>Compiler</b> — A language translator program that condenses usercreated high-level language programs into something the computer can execute directly in its binary circuitry. See also Interpreter. Some compilers perform syntax abbreviation as an intermediate step and make use of an associated run-time program to perform the final translation of user program to executable binary code.
<b>CPM</b> — Acronym for Control Program Monitor. One of the most popular microcomputer operating systems.
CDII - Saa Cantral Processing Unit

61

**CRT** — Acronym for Cathrode Ray Tube; a computer video display terminal (not suitable for ordinary TV reception).

**Cursor Control** — Ability to move a video display prompt character to any position on the screen, under either keyboard or software command.

**Data** — Information; often numerical information.

**Debug** — Correct errors.

**Digital Computer** — Calculates with discrete numerical information, as opposed to Analog Computer.

**Disk** — See Floppy Disk.

**Disk Drive** — Electromechanical mass storage unit.

**Diskette** — See Floppy Disk.

**DMA** — Direct Memory Access. A method by which data can be transferred between peripheral devices and internal memory without intervention by the central processor.

**DOS** — Disk Operating System. A sophisticated monitor capable of accessing, managing and servicing files and data stored on floppy and hard disk subsystems.

**Driver** — A software driver is a series of instructions the computer follows to reformat data for transfer to and from a particular peripheral device. The electrical and mechanical requirements are different from one kind of device to another and the software drivers are used to standardize the format of data between them and the central processor.

**Dumb Terminal** — A computer peripheral without any intelligence of its own. The sole function of the terminal is to translate between human visual symbols and keystrokes and the binary language of the computer.

**Dynamic Memory** — RAM memory that needs to be refreshed every few milliseconds. Most dynamic RAM boards have on-board refresh logic that relieves the central processor of this tedious task.

**Firmware** — Programming built into the computer to make its operation simpler for the user to understand. Firmware is usually supplied by the manufacturer stored in PROM memory units. See Monitor.

**Fixed Point** — A convention used to represent non-integer numbers in the computer.

**Floating Point** — A convention for representing non-integer numbers in the computer using scientific exponential notation.

**Floppy Disk** — A flexible plastic disk coated with the same magnetic material used to make recording tape. The disk stores computer information on fifty or more tracks around its surface.

**Flow Chart** — A diagram of geometric shapes connected by arrows that show the progression of a computer program. Flow charts are handy for developing complicated computer programs and illustrating how programs work.

**Foreground/Background** — A priority system through which current users' needs are serviced first and other tasks are completed more slowly.

**Gate** — A very simple electronic circuit that is always either **on** or **off.** Clusters of gates can manipulate binary numbers (0 = off; 1 = on). They can also count, do arithmetic, make decisions and store binary numbers. Gates are the basic building blocks of computers.

**Handshaking** — Interaction between the central processor and peripheral devices. The devices report their status during data transfers so the processor knows when the operation is completed and more data can be transferred.

**Hard Copy** — The permanent printout of a program or its results produced by a printer connected to a computer.

**Hard Sector** — Magnetic floppy discs are divided into wedges called sectors which are physically marked by holes punched through the disc to indicate the various sectors. Soft sectoring is a method of determining position of data on the disc by software calculations rather than physical monitoring of the disc.

**Hardware** — The circuit boards and electronic parts inside a computer.

**Hexadecimal** — A number system based on powers of 16, and having sixteen digits usually numbered 0 thru 9 and then A thru F. Decimal 178 is represented as B2 in hexadecimal notation. Note that an 8-bit byte can be expressed in two hexadecimal digits.

**Input** — The means by which data is entered into a computer. Often a keyboard.

**Instruction** — A statement or command that tells a computer what to do.

Integer — A whole number, positive, negative, or zero.

**Interface** — A circuit that controls the flow and format of data between the central processor and peripheral devices.

**Interpreter** — The program stored inside a computer that converts BASIC statements into the computer's machine language.

**Interrupt** — A signal that interrupts a running program so that some other task can be performed. Sometimes interrupts are given priorities so that the central processor will suspend its current task only if the priority is great enough for immediate execution.

**I/O** — Input/Ouptut of information in a computer system.

**K** — Short for **kilo** meaning thousand. Used to designate memory capacity; thus a 4K memory has approximately 4,000 storeage elements.

**Keyboard** — A typewriter-like panel of switches and keys used to enter programs and data into a computer.

**Language** — A system of programming instructions easily understood by both the programmer and the computer. A programming language has rules of syntax, which must be followed when writing instructions to the computer. These instructions are translated into machine language instructions, which are sequences of binary numbers and are based on the microprocessor's internal circuitry. Since the circuitry varies from one microprocessor to another, the binary codes are frequently incompatible. The solution to this problem is the *higher level language*, such as BASIC. With minor variations, a BASIC program written for one computer is understandable to another. This makes it easier for people to share their programing efforts with others.

**Line Printer** — See Hard Copy.

**Linking Loader** — An executive program which connects different program segments so they may be run in the computer as one unit. A useful piece of software that makes subtasks easily available to a main task.

**Macro-Assembler** — An Assembler that allows the user to create and define new computer instructions (called macros).

**Mainframe** — The box with power supply, motherboard and (optional) front panel, into which various printed circuit boards may be plugged.

**Megabyte** — A million keystrokes; a million characters.

**Memory** — Any of the many devices (ROMs, RAMs, floppy disks, magnetic tapes, etc.) that store computer programs and data.

 $\mathbf{MHz}$  — Megahertz. One Hertz equals one cycle per second. Notation for the frequency or clock speed of various integrated circuits, e.g., the 8080A operates at 2 MHz and the Z-80 at 4 MHz.

**Microprocessor** — The central processing unit of a computer assembled on a single silicon chip.

**Microcomputer** — A computer made by combining a microprocessor with some memory. Microcomputers are small in size, not performance.

**Modem** — Electronic equipment hard-wired into a telephone line to facilitate connection and disconnection between a computer and remote peripherals.

**Monitor** — A small package of software usually stored on PROMs that gives the computer a fundamental interactive intelligence. The Monitor usually contains software routines and I/O drivers needed by the user to operate the system. The Monitor is a kind of executive secretary for the user. It tells the computer how and where to acquire the programs and data, where to store them, and how to run them.

**Motherboard** — The central communications bus line. The spinal cord of a microcomputer.

**NRZ** — Non Return to Zero, one of several methods for coding digital information on magnetic tape.

**Operating System** — A sophisticated monitor often found with floppy disc systems.

**Output** — The means by which data leaves a computer. Often a television monitor or printer.

**Paper Tape** — A narrow ribbon of paper which contains computer data in the form of punched holes. A hole indicates the bit 1; no hole indicates the bit 0. Paper tape is sometimes used to enter programs into a computer.

**Parallel** — A type of interface in which all bits of data in a given byte are transferred simultaneously, using a separate data line for each bit.

**Parity Bits** — Used to ensure integrity of data transmitted along communications lines.

**Peripheral** — An accessory which can be added to a computer to increase its capability and usefulness (a floppy disk, paper tape unit, etc.)

**Personal Computer** — An economical microcomputer designed for use by small businesses, schools and computer hobbyists.

**Port** — The physical communication line between the central processor and a peripheral. Each port has a numerical address that the processor uses in communicating through it. 8080/Z-80 microprocessors can address up to 256 input and 256 output ports.

**Printed Circuit** — Electrical connections of solder traces between components on an epoxy board material.

**Printer** — A computer output mechanism that delivers hard copy data.

**Program** — The list of instructions or statements that tells a computer what to do.

**Program Cartridge** — Read Only Memory on a printed circuit board enclosed in a cartridge case.

**Programmer** — A person who writes programs. Usually a professional.

**PROM** — Programmable Read Only Memory. This is computer memory which does not forget what it knows, even when the power is shut off. Some kinds of PROM can be erased and reused: EPROMs, or Erasable PROMs. PROMs are a convenient way for the user to design his own operating system software and other tailor-made monitor routines.

**Prompt** — A character displayed by a computer as a signal that it expects a response.

**RAM** — (**R**andom **A**ccess **M**emory) A temporary memory, i.e., one in which data can be stored so long as power is applied. RAMs store the data that's typed into the keyboard of a microcomputer.

**Real-Time Clock** — A piece of hardware which interrupts the processor at fixed time intervals to synchronize the operations of the computer with events occurring in the outside world, often involving man/computer interaction.

**Reentrant Code** — Assembler-generated machine language programs that may be shared simultaneously by any number of users.

**Refresh** — A signal sent to dynamic RAM every few milliseconds to help it remember data.

**Relocatable Code** — Assembler-generated machine language programs that may be placed anywhere in available computer memory for execution. Relocatable code makes life easier for the small system user who does not possess a full complement of memory boards.

**ROM** — (**R**ead **O**nly **M**emory) A permanent memory, i.e., one in which data is stored permanently whether or not electrical power is applied.

**ROM PAC<sup>TM</sup>** — Read Only Memory programmed and packaged on a printed circuit card inside a plastic cartridge.

**RS232** — An industry-wide standard protocol for serial communication between computers and peripheral devices.

**Serial** — A type of interface in which all the bits of data in a given byte are lined up sequentially (usually with start, stop and parity-error checking bits at the head and tail of the byte) for transfer along one data line.

**Smart Terminal** — A computer peripheral capable of computing functions on its own. Smart terminals can usually be switched on command to dumb terminal mode for conversation with larger computers.

**Software** — Computer programs written on paper or stored on magnetic tape or a floppy disk.

**Standard BASIC** — A set of easy English word instructions used to program a microcomputer.

**Statement** — A single line of a computer program containing a single instruction like PRINT, LET, RUN, etc.

**Static Memory** — RAM memory that holds information with high reliability as long as power is applied. More expensive than dynamic RAM.

**String** — A group of data elements (usually ASCII characters) stored in sequential memory locations and treated as one unit for I/O operations, text editing and other program manipulations.

**System** — A complete, integrated and functional computer made up of various hardware components linked harmoniously together and unified by software-programmed intelligence.

**Telecommunications** — Communication between computers and peripheral devices over telephone lines.

**Terminal** — An input device such as a keyboard; an output device such as a printer or TV monitor; or both.

**Timesharing** — A computer system that seems to be performing multiple tasks for a number of users simultaneously. In actuality, the processor is working for only one user at any particular moment, but has been programmed to remember what each user was doing last.

**Turn-key** — A computer system ready to perform all tasks the moment you turn it on. Business and accounting software is frequently supplied in ready-to-run form on such a system.

**User's Group** — An informal or formal association of persons who own or operate similar or identical computing equipment. User's groups are usually formed to exchange programs and other helpful information.

**Vectored Interrupt** — A procedure by which interrupts force the central processor to transfer program control to a particular pre-stored routine that handles and processes the interrupt. **Virtual Memory** — A method by which maxis and minicomputers

**Virtual Memory** — A method by which maxi- and minicomputers appear to the user to have unlimited RAM memory resources. Hard disk storage space is used to extend the mainframe RAM through sophisticated operating system program segmentation techniques.

# APPENDIX E ASCII CHARACTER CODE

ASCII stands for American Standard Code for Information Exchange.

# Decimal and Hexadecimal Designations of the Standard Characters



DEC.	HEX.	CHAR.	DEC.	HEX.	CHAR.	DEC.	HEX.	CHAR.
000	00	NUL	031	1F	US	062	3E	>
001	01	SOH	032	20	SPACE	063	3F	?
002	02	STX	033	21	1	064	40	@
003	03	ETX	034	22	"	065	41	Α
004	04	EOT	035	23	#	066	42	В
005	05	ENQ	036	24	\$	067	43	С
006	• 06	ACK	037	25	%	068	44	D
007	07	BEL	038	26	&	069	45	Е
800	08	BS	039	27	•	070	46	F
009	09	HT	040	28	(	071	47	G
010	0A	LF	041	29	)	072	48	Н
011	0B	VT	042	2A	*	073	49	I
012	OC	FF	043	2B	+	074	4A	J
013	0D	CR	044	2C	,	075	4B	K
014	0E	SO	045	2D	-	076	4C	L
015	0F	SI	046	2E		077	4D	M
016	10	DLE	047	2F	1	078	4E	N
017	11	DC1	048	30	0	079	4F	0
018	12	DC2	049	31	1	080	50	P
019	13	DC3	050	32	2	081	51	Q
020	14	DC4	051	33	3	082	52	R
021	15	NAK	052	34	4	083	53	S
022	16	SYN	053	35	5	084	54	T
023	17	ETB	054	36	6	085	55	U
024	18	CAN	055	37	7	086	56	V
025	19	EM	056	38	8	087	57	W
026	1A	SUB	057	39	9	088	58	X
027	1B	ESC	058	3A	:	089	59	Y
028	1C	FS	059	3B	;	090	5A	Z
029	1D	GS	060	3C	<	091	5B	[
030	1E	RS	061	3D	=	092	5C	1

DEC.	HEX.	CHAR.	DEC.	HEX.	CHAR.	DEC.	нех.	CHAR.
093	5D	1	105	69	i	117	75	u
094	5E	^	106	6A	, <b>j</b>	118	76	v
095	5F	<	107	6B '	k	119	77	w
096	60	•	108	6C	1	120	78	x
097	61	a	109	6D	m	121	<b>7</b> 9	y
098	62	ь	110	6E	n	122	7A	Z
099	63	С	111	6F	o	123	7B	{
100	64	d	112	70	p	124	7C	ĺ
101	65	е	113	71	q	125	7D	}
102	66	f	114	72	r	126	7E	~
103	67	g	115	73	s	127	7F	DELETE
104	68	h	116	74	t			(Rubout)
		obreviati 1F Hex.		r ASCI	I charac	ters O 1	throug	h 31
ACK	= Ack	nowledg	e		FF = Fo	orm Fe	ed	
BELI	. = Be	11			FS = Fc	orm Se	parator	
	= Backs				GS = G		-	

BS = BackspaceGS = Group Separator HT = Horizontal Tab CAN = CancelLF = Line Feed CR = Carriage Return DC1 = Direct Control 1 NAK = Negative Acknowledge NUL = NullDC2 = Direct Control 2 RS = Record Separator DC3 = Direct Control 3 DC4 = Direct Control 4 SI = Shift In

DLE = Data Link Escape

EOT = End Of Transmission

EM = End of Medium

ENQ = Enquiry

ESC = Escape

ETX = End Text

US = Unit Separator ETB = End Transmission Block VT = Vertical Tab

SO = Shift Out

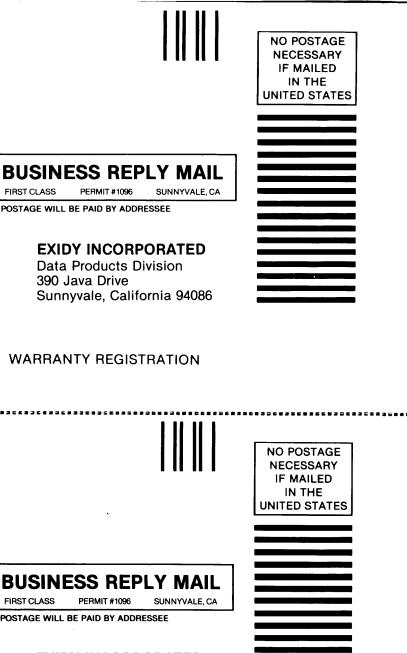
STX = Start TextSUB = Substitute

SOH = Start of Heading

SYN = Synchronous Idle

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